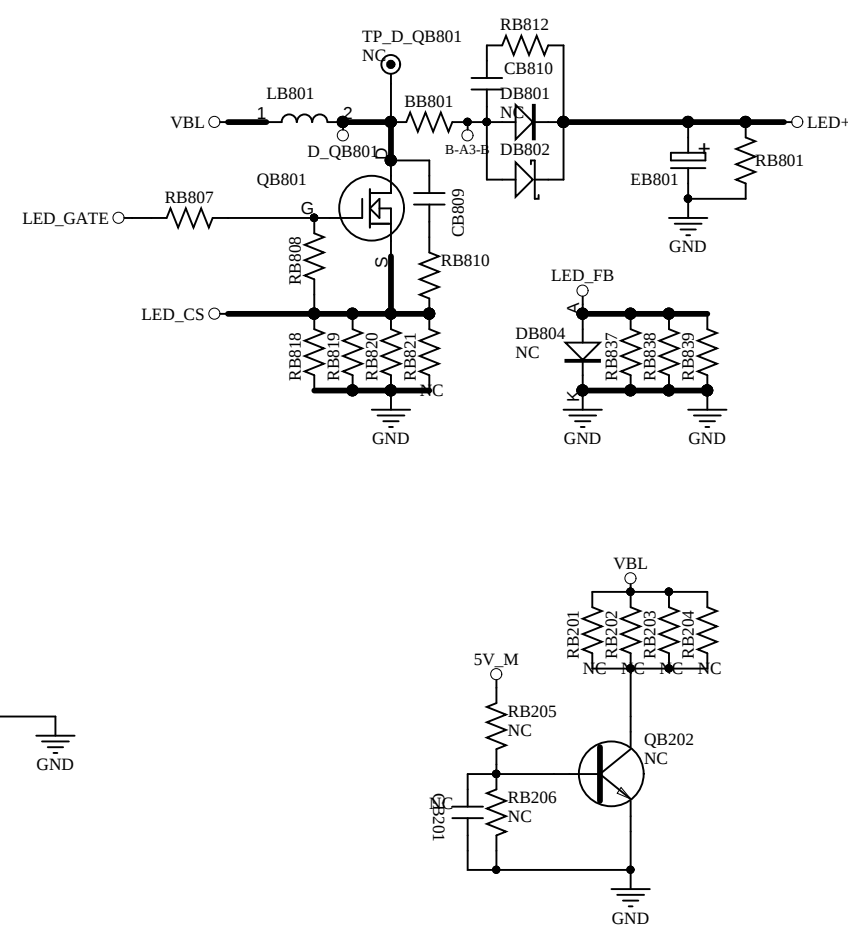
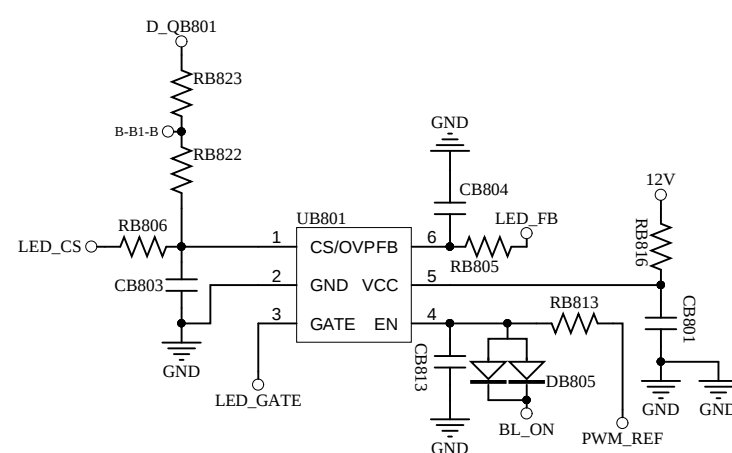
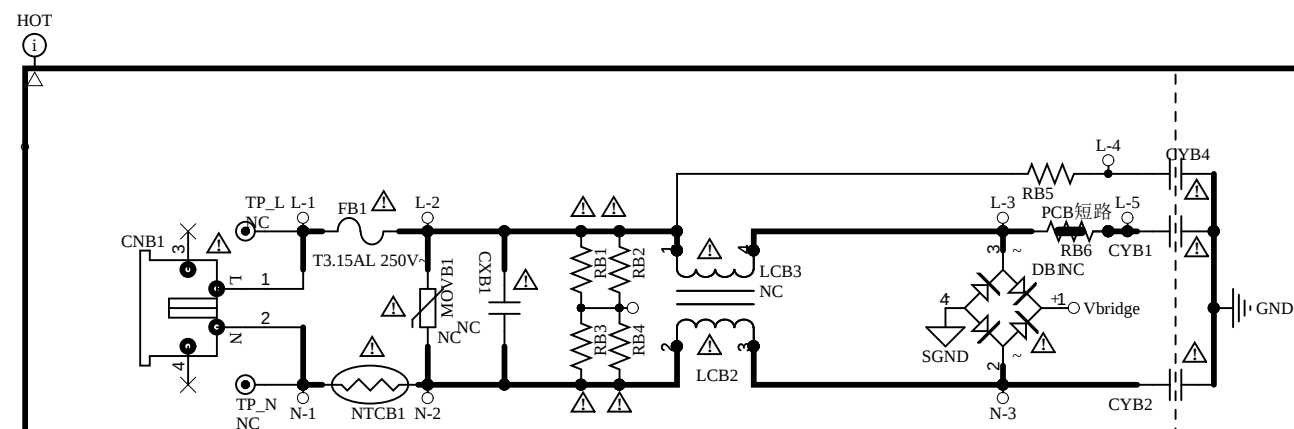
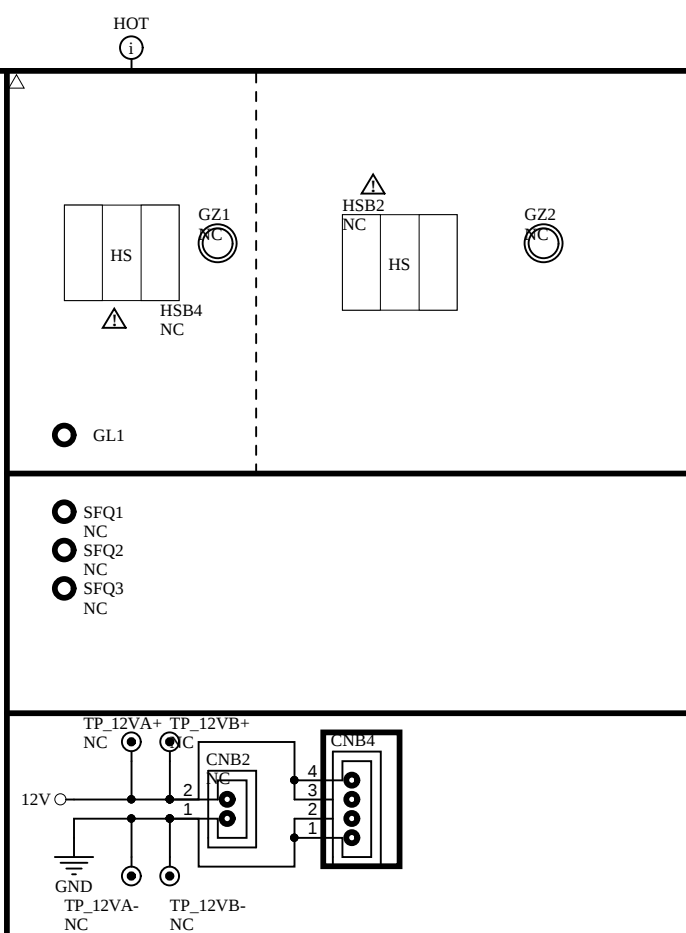
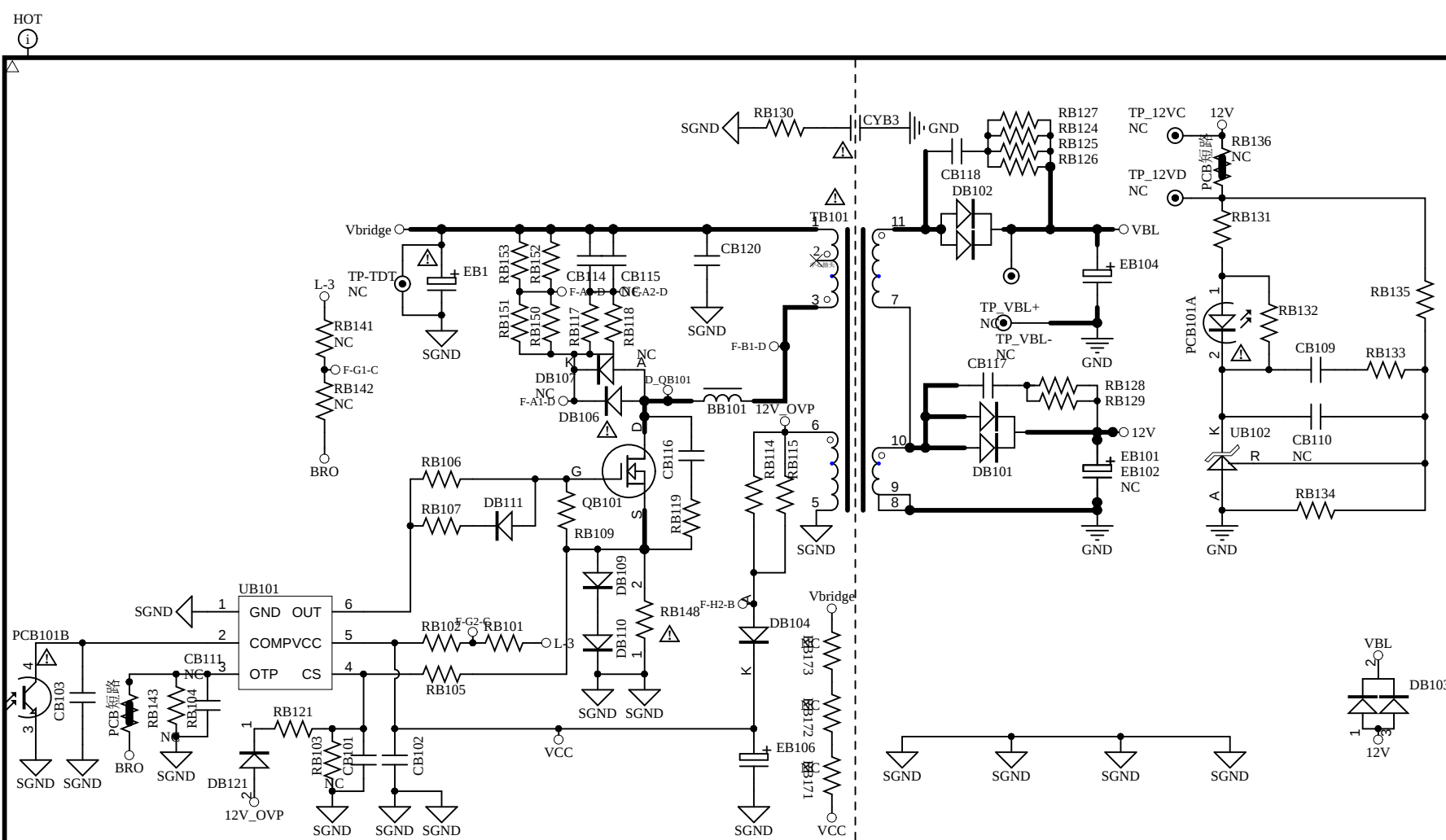
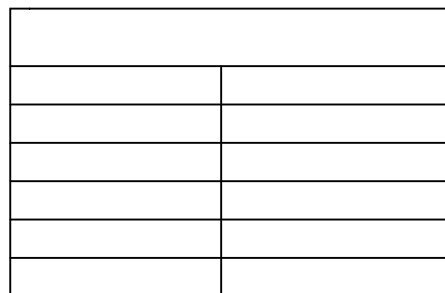
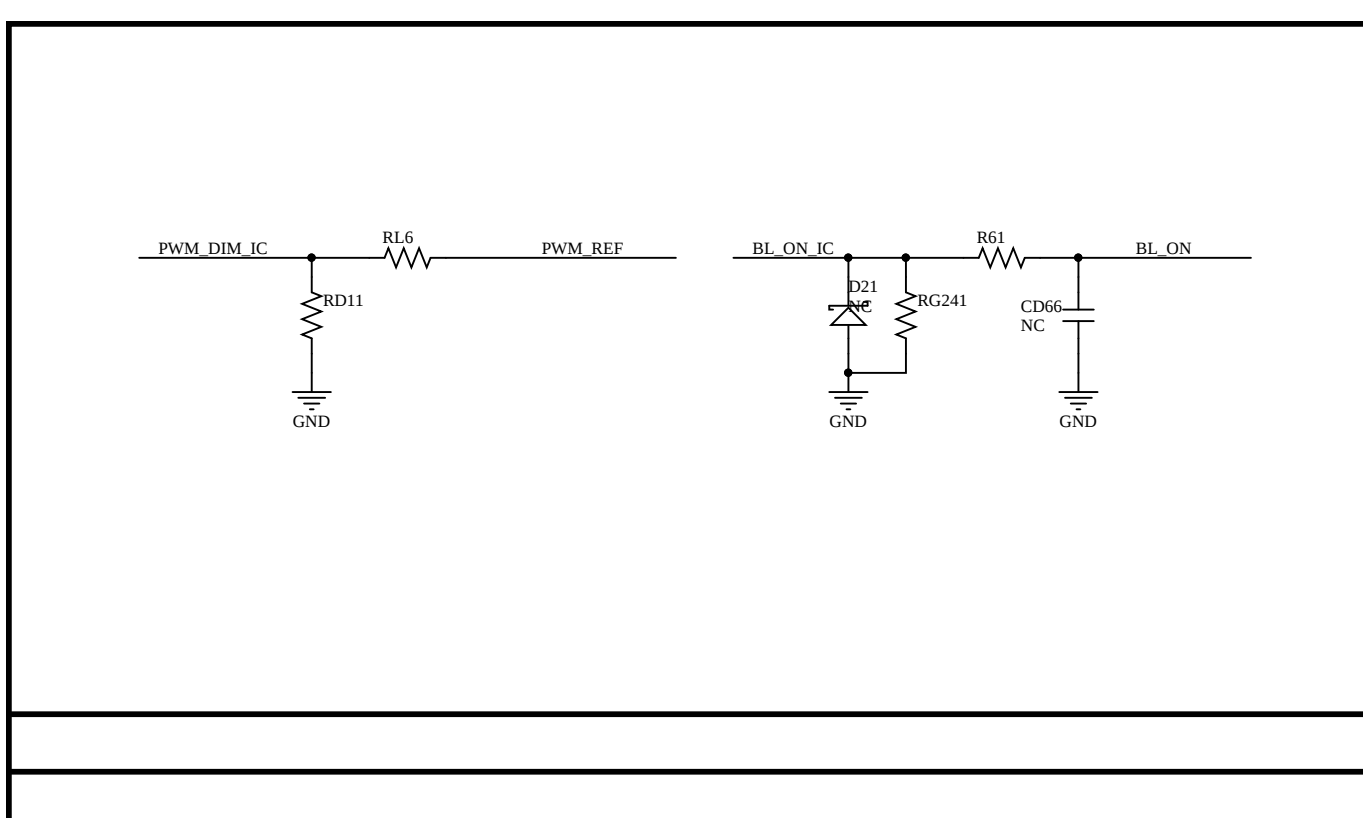
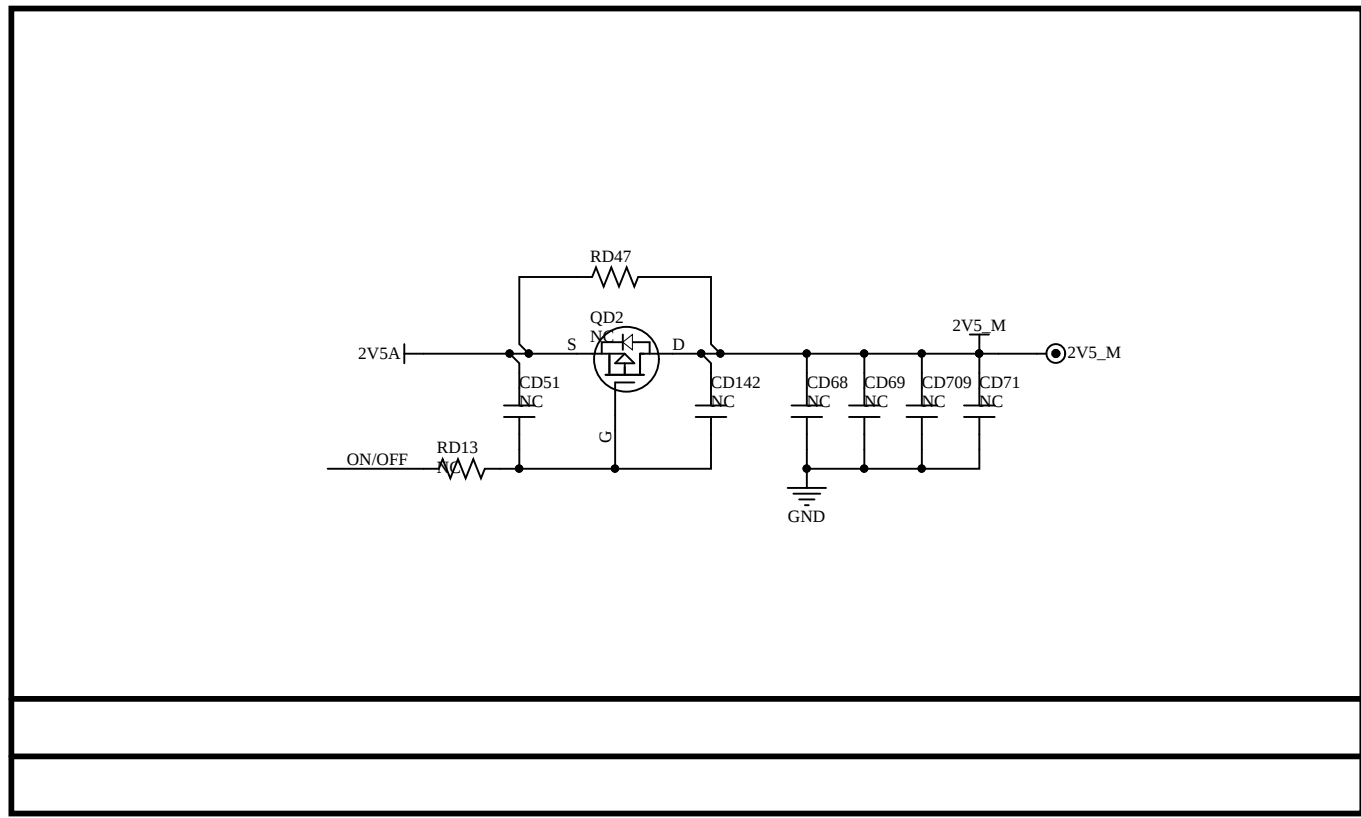
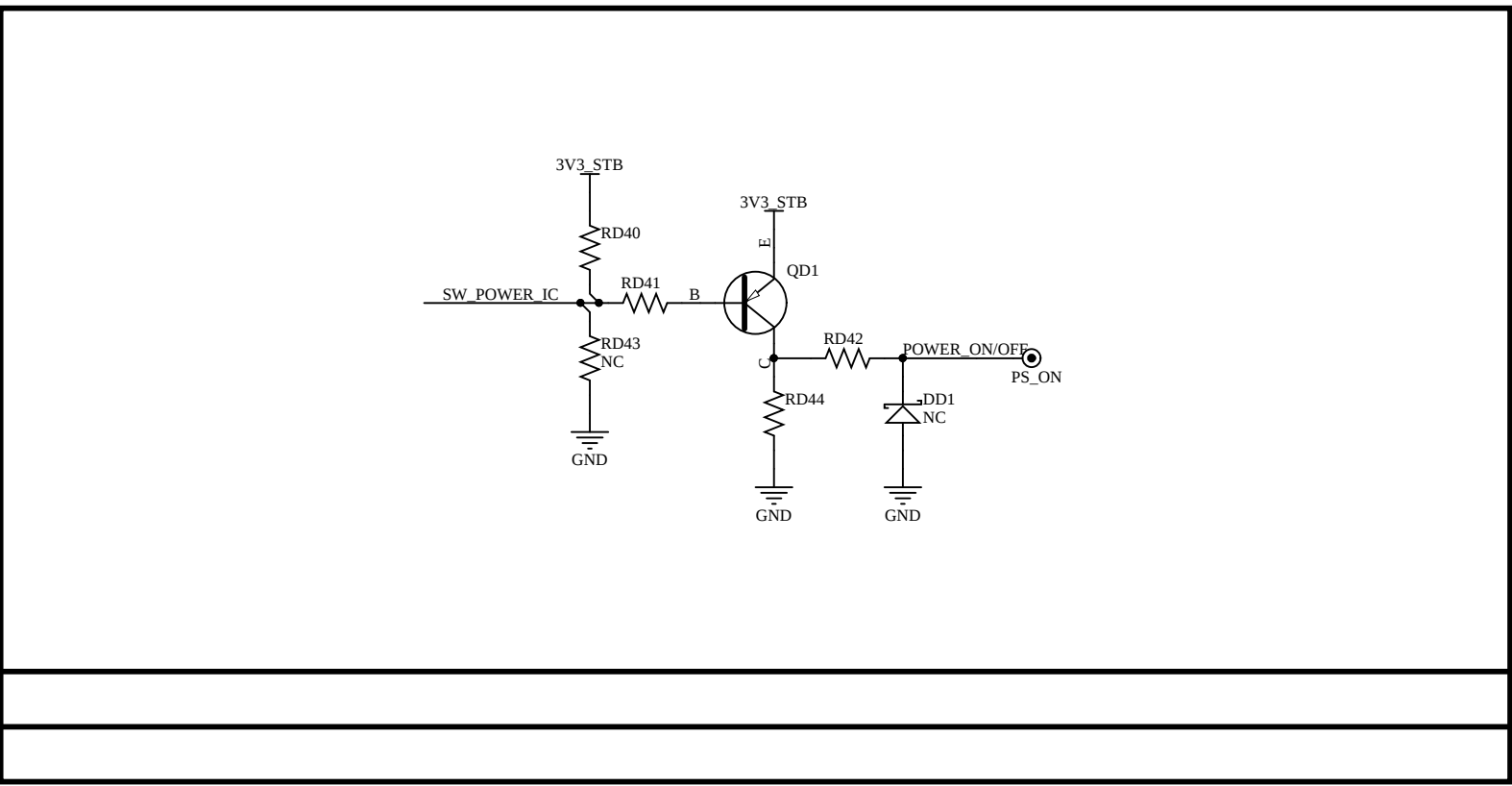
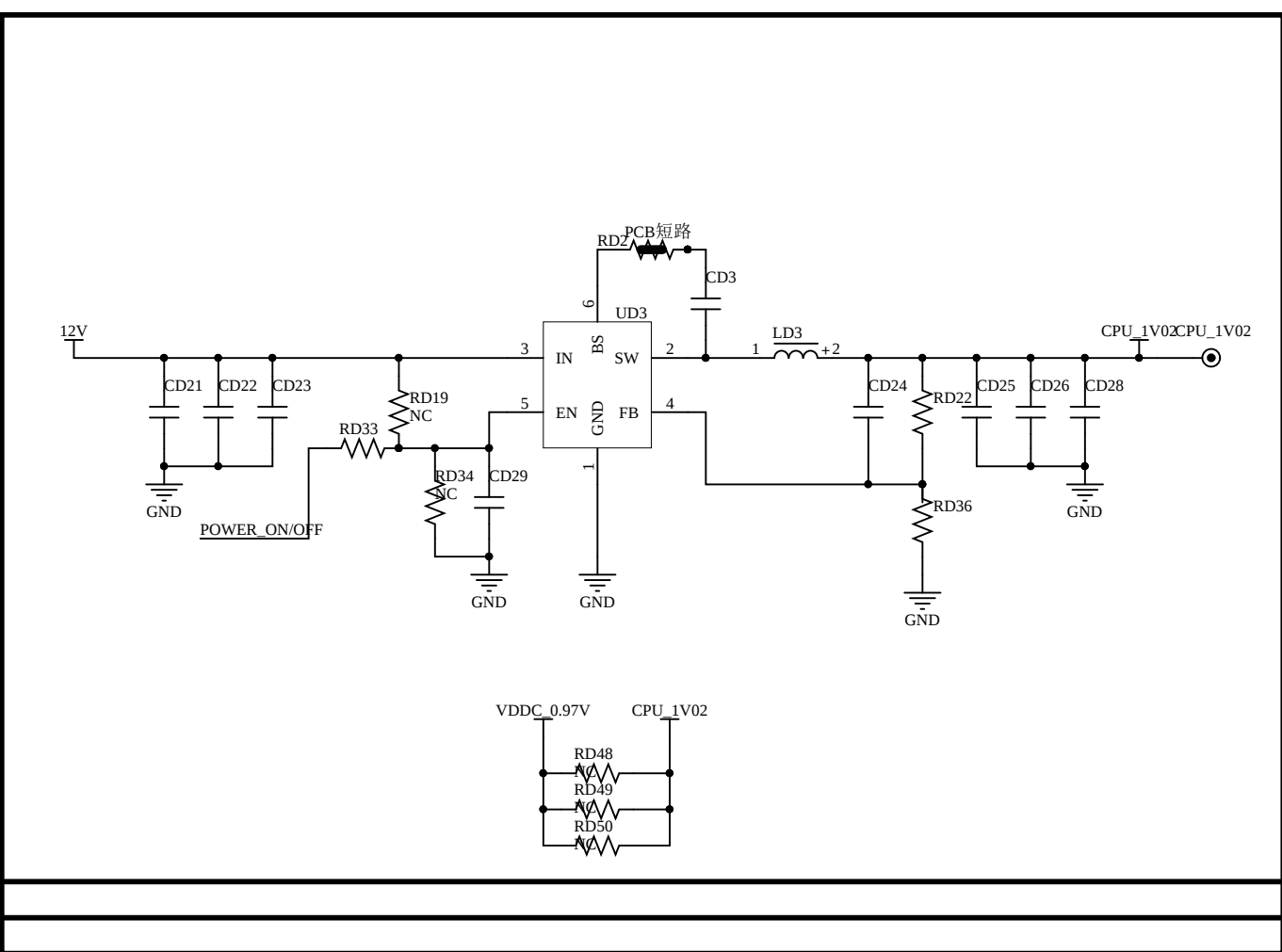
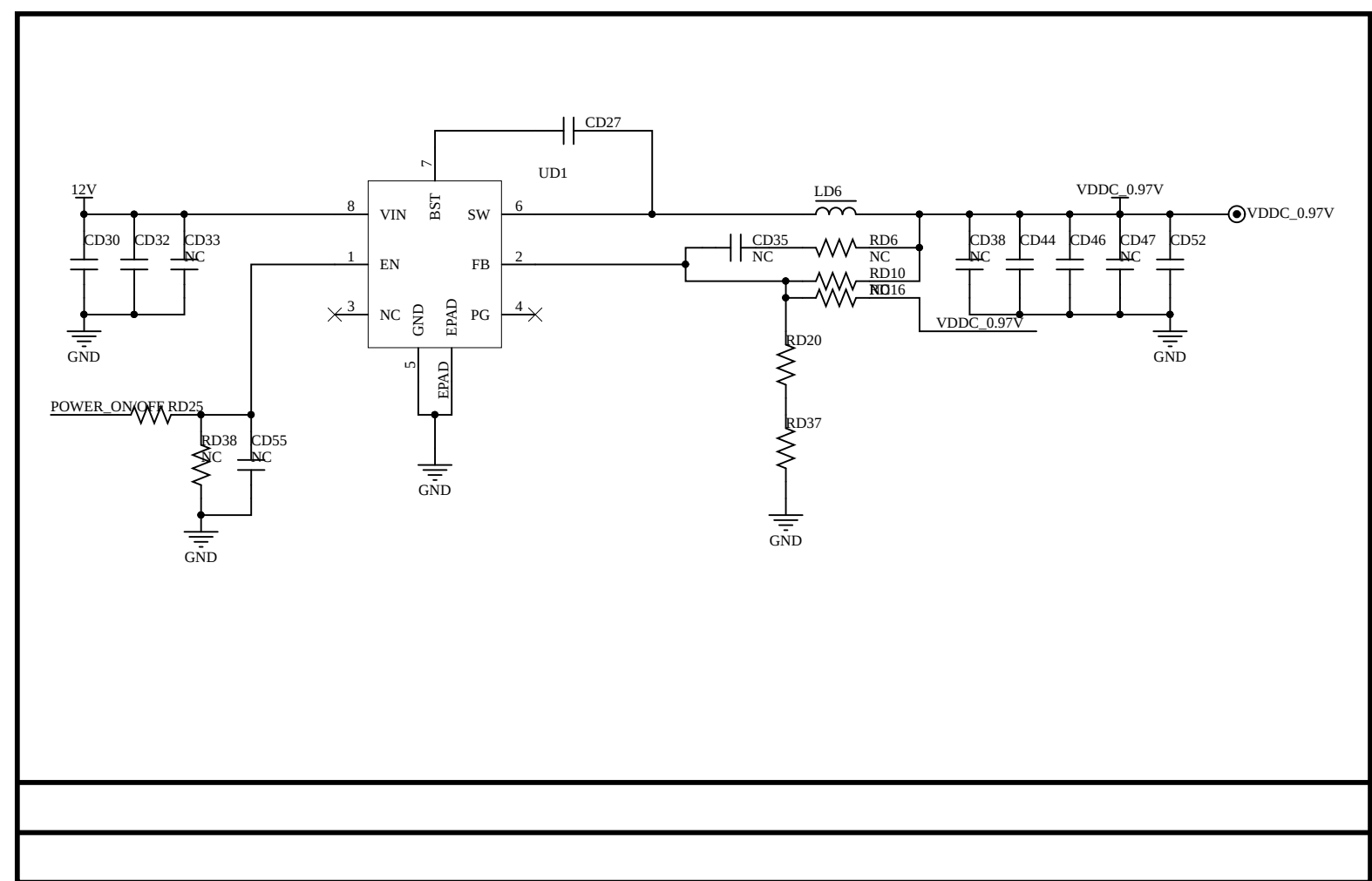
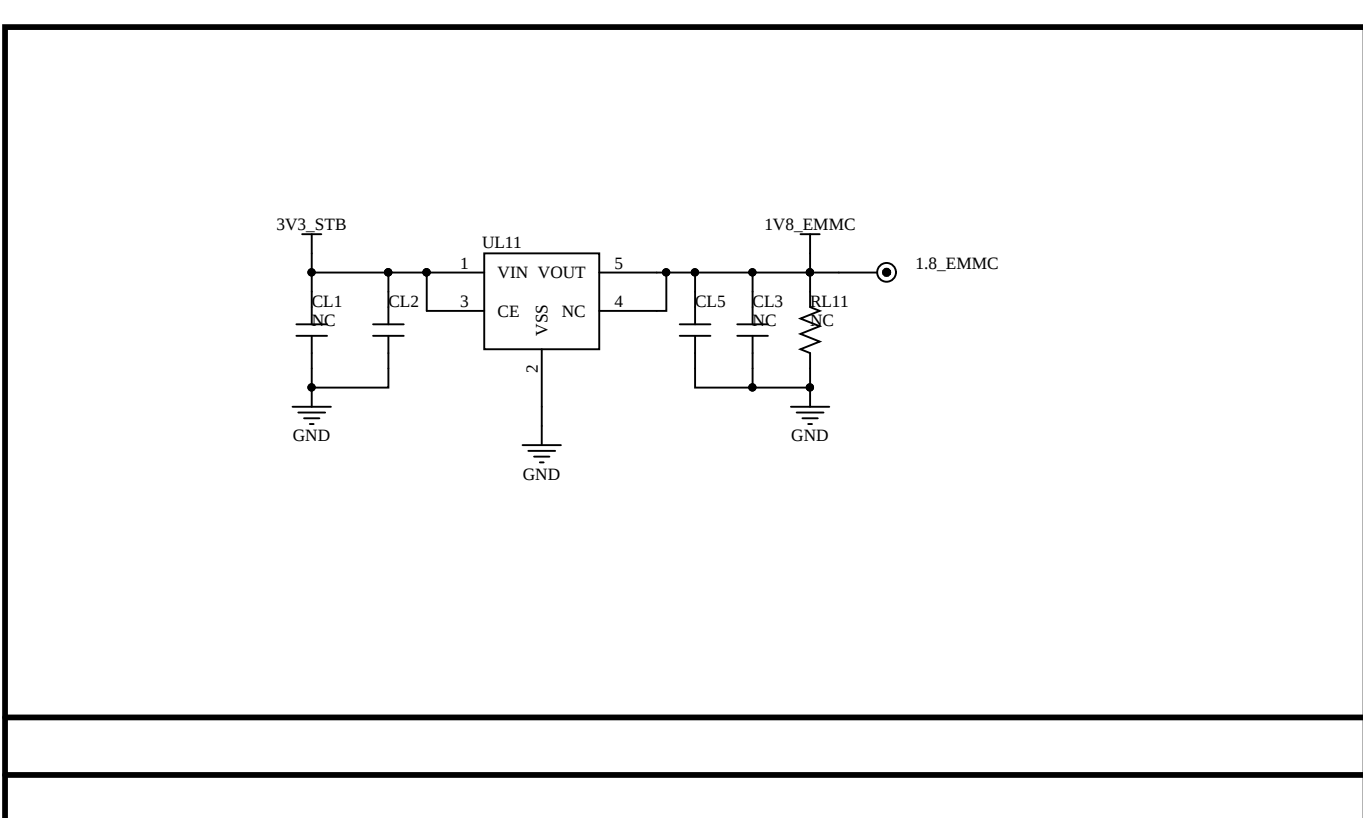
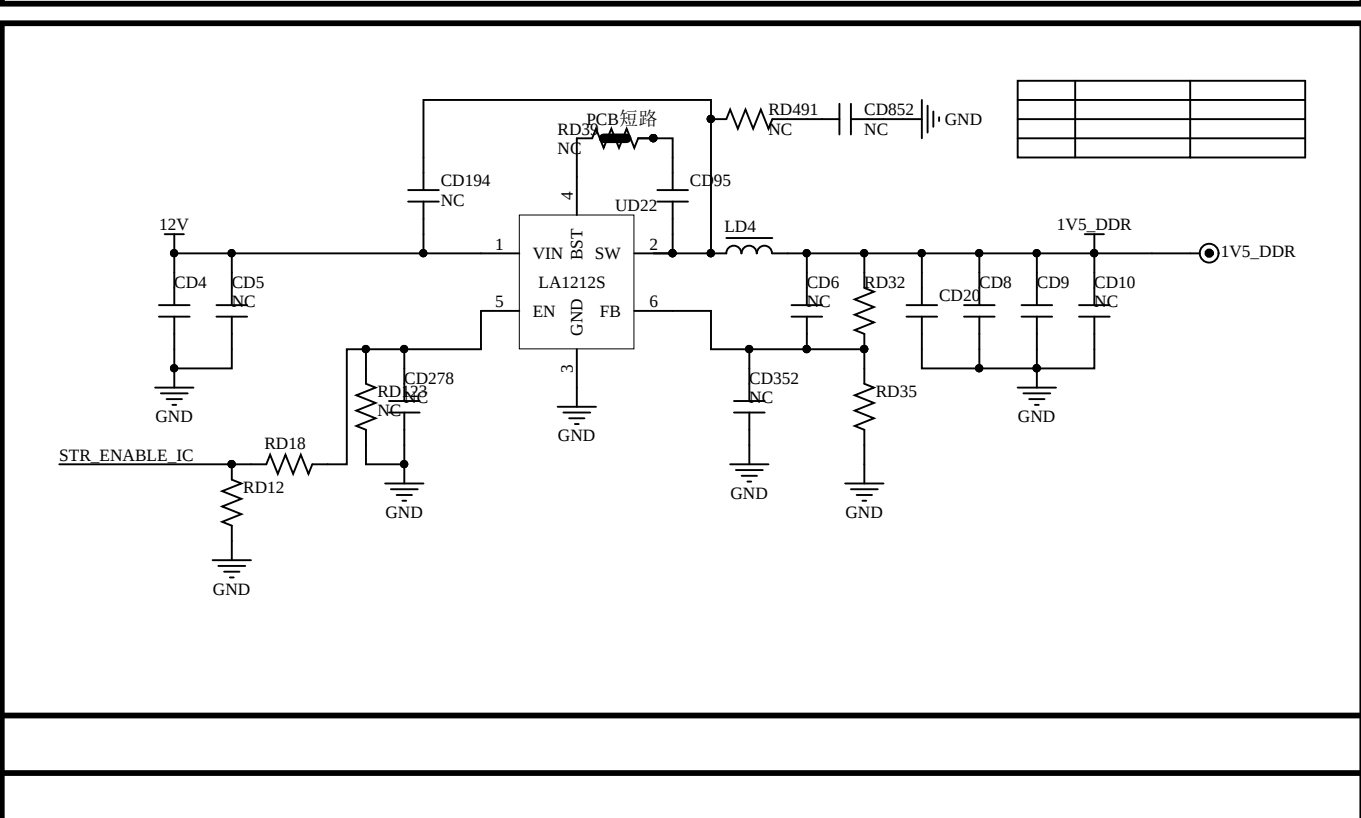
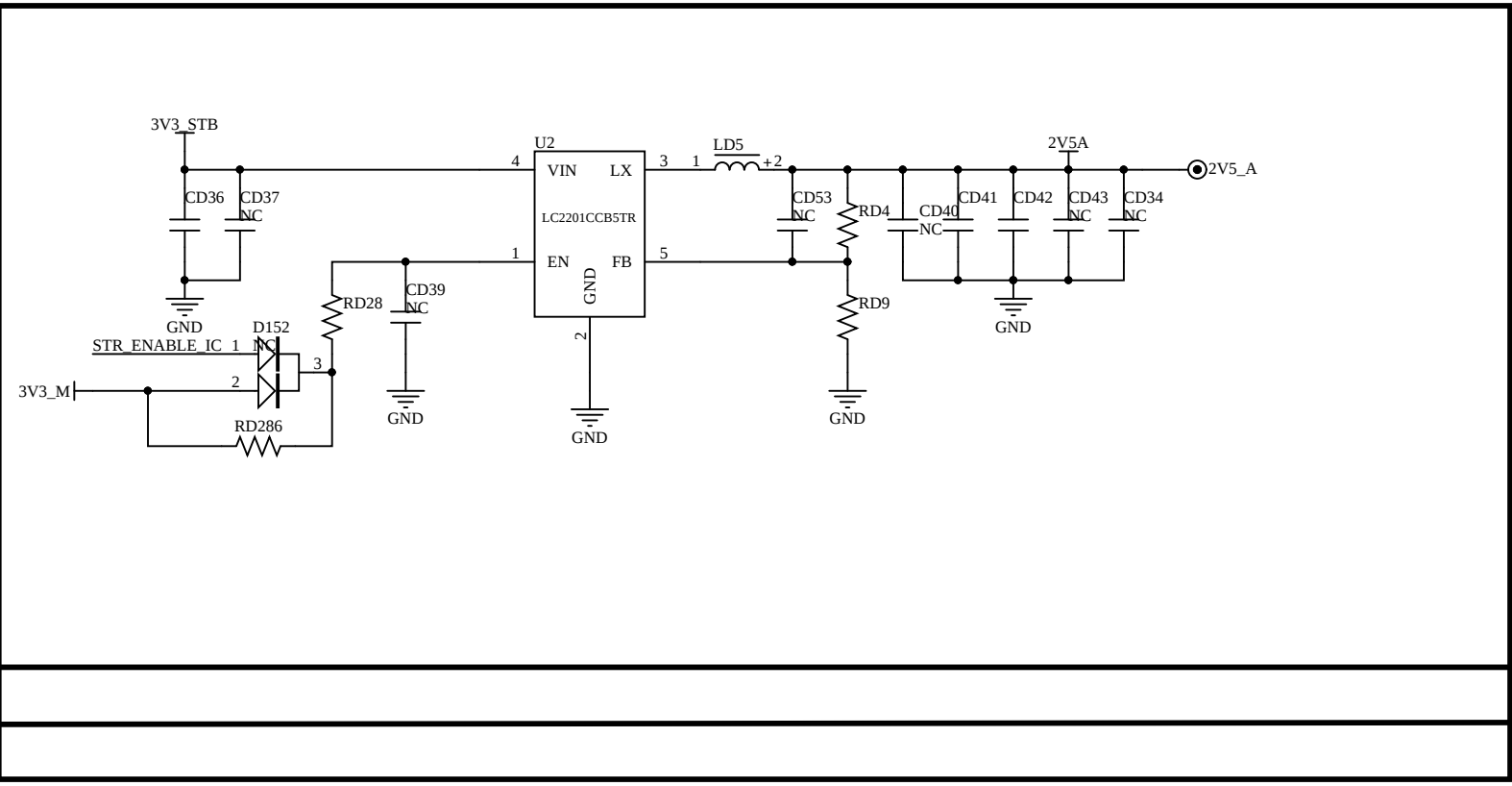
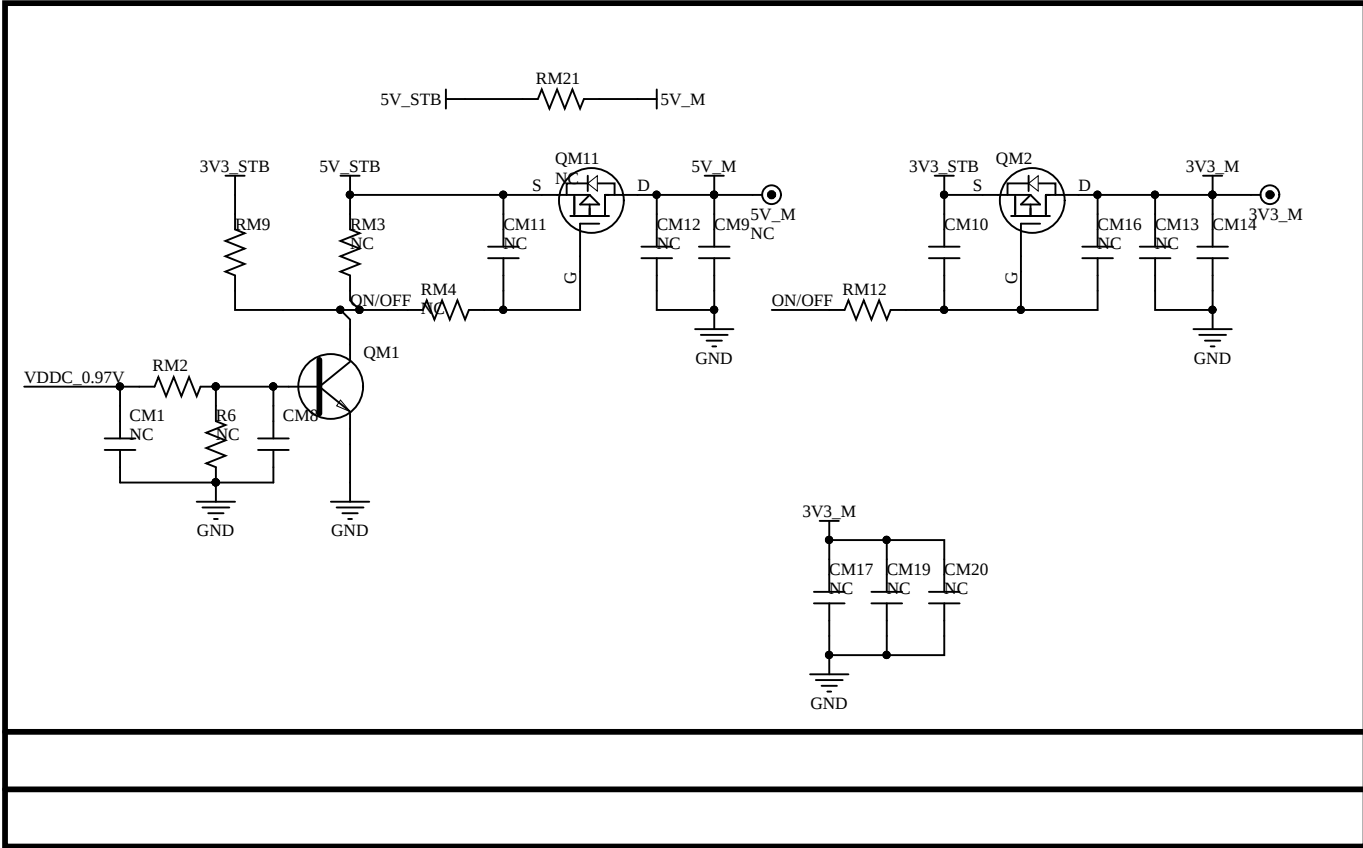
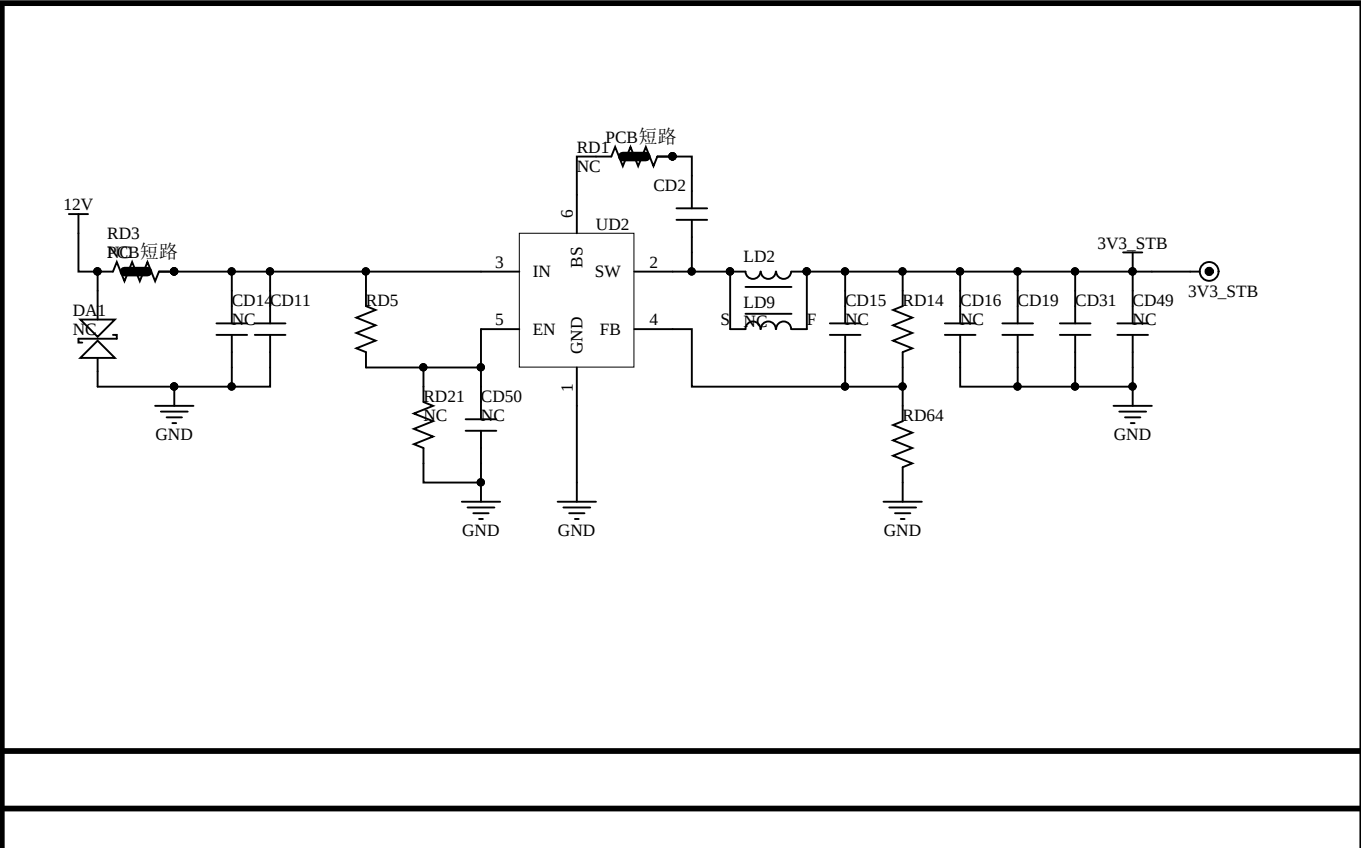
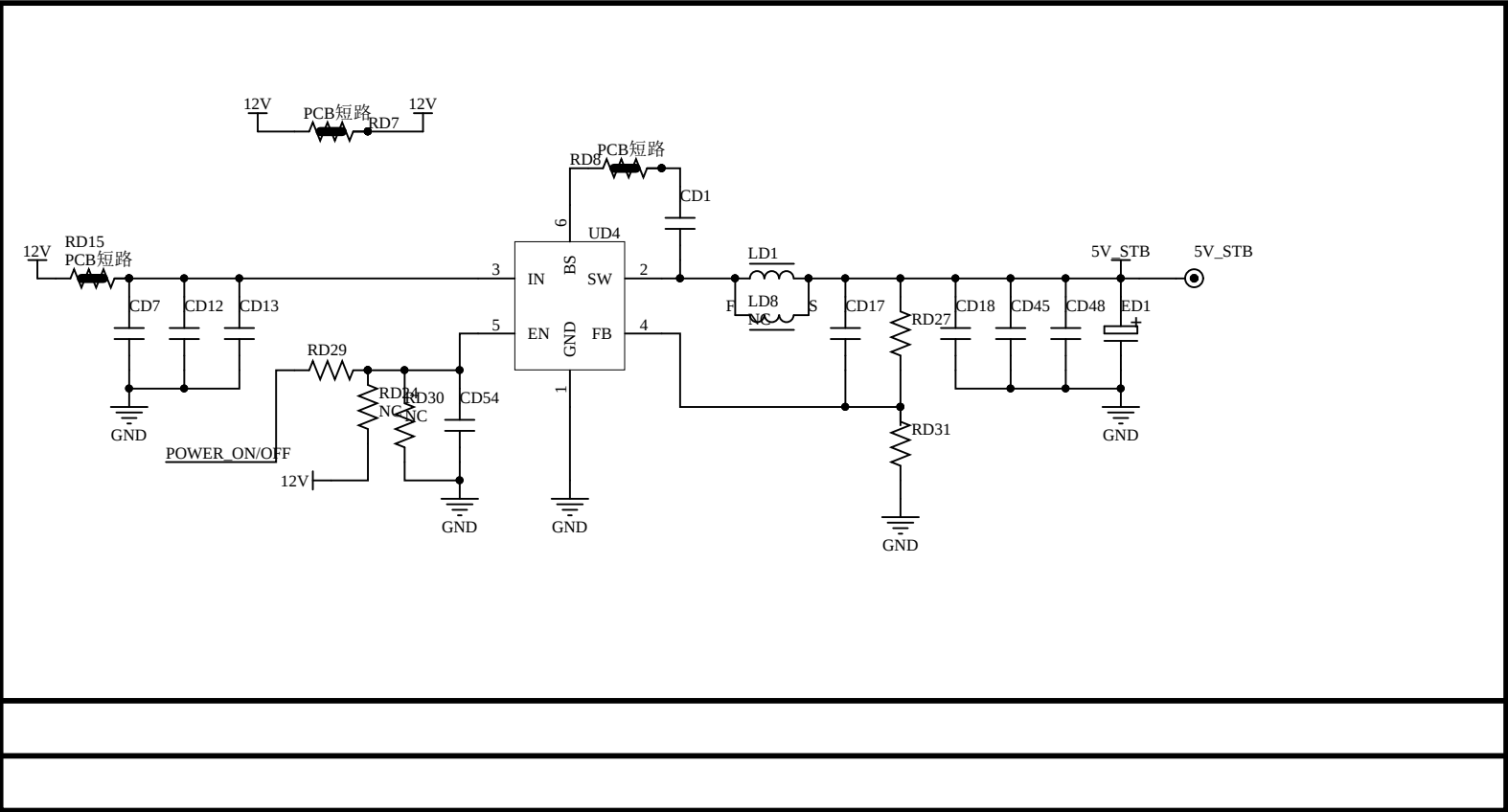
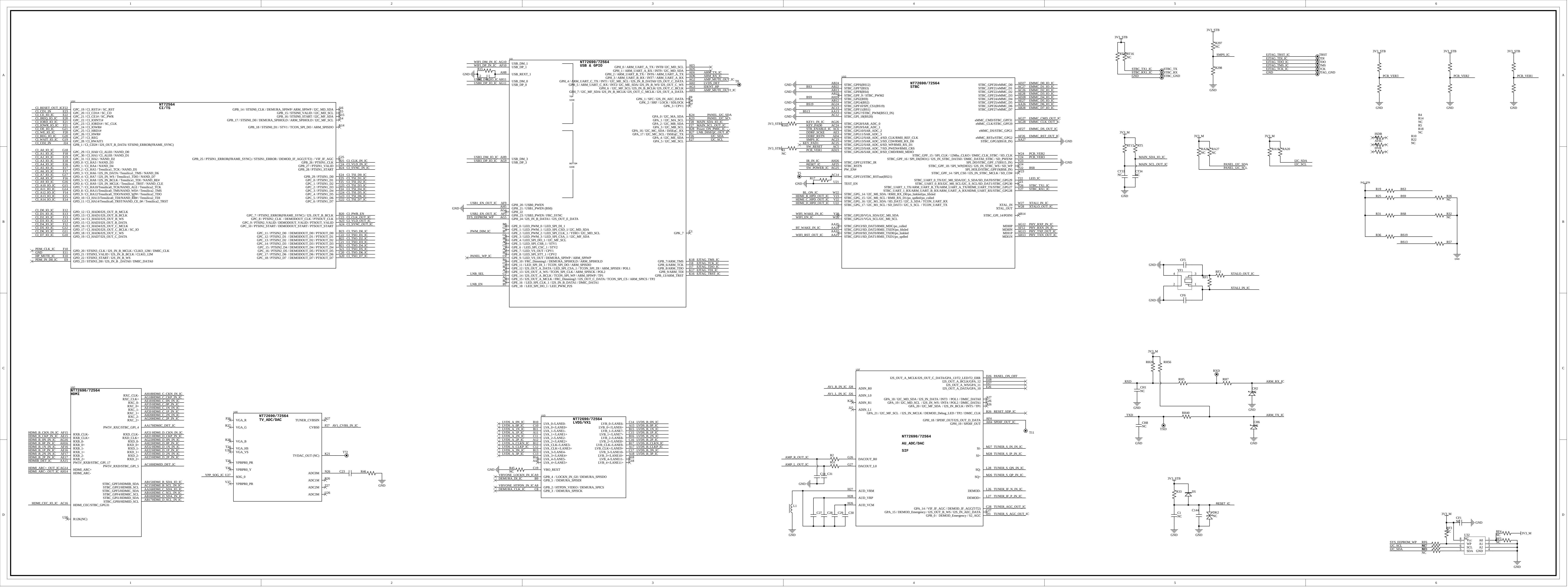
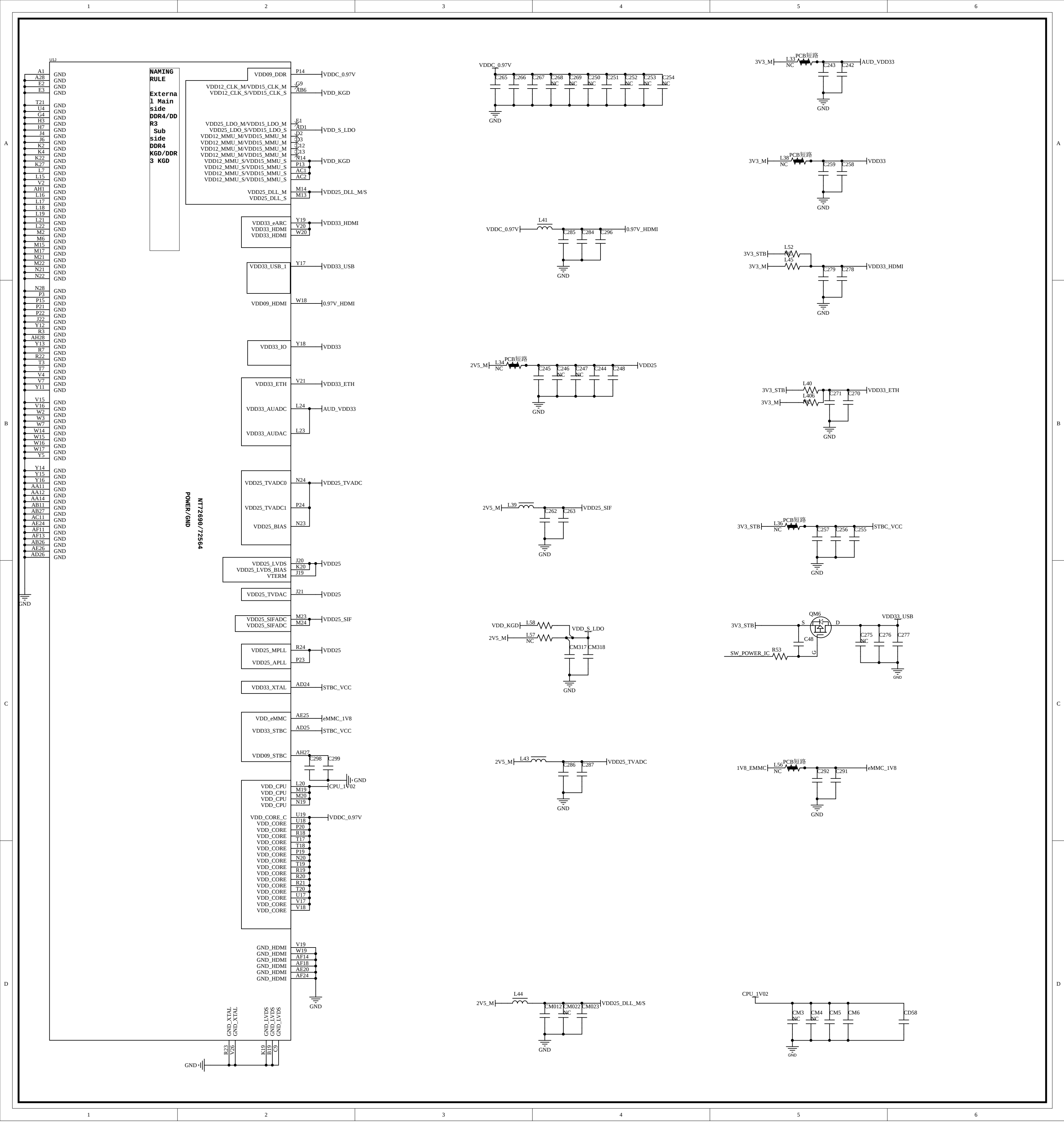


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B						
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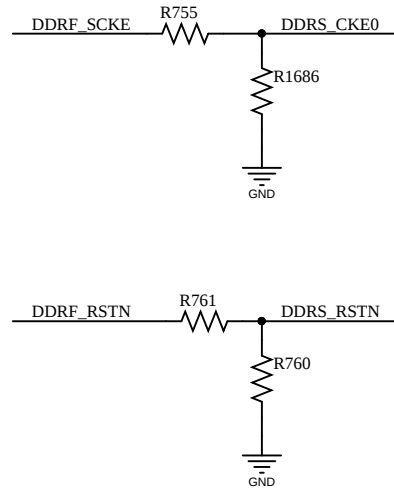
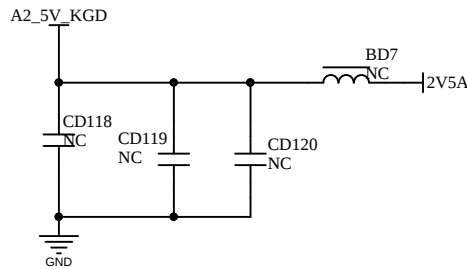
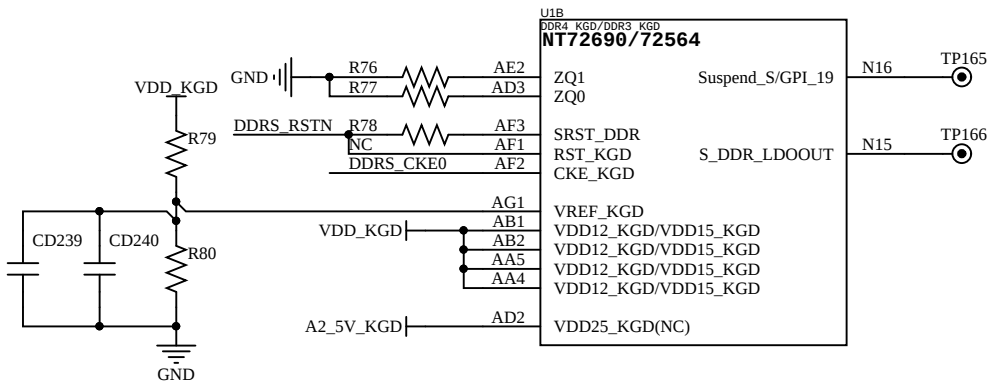
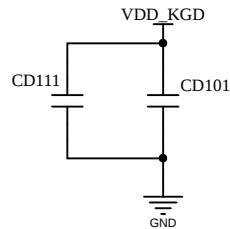
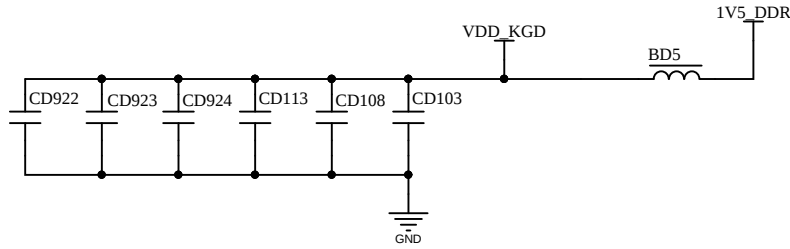
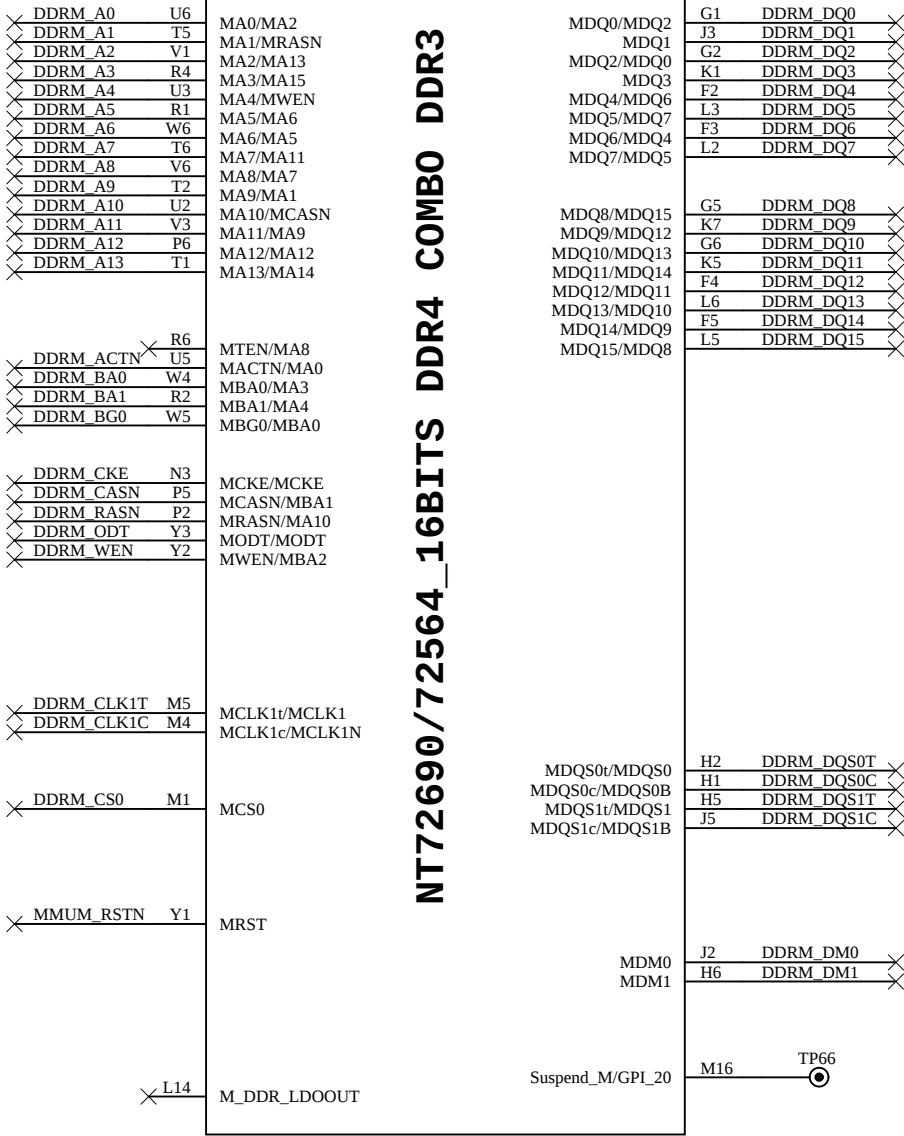


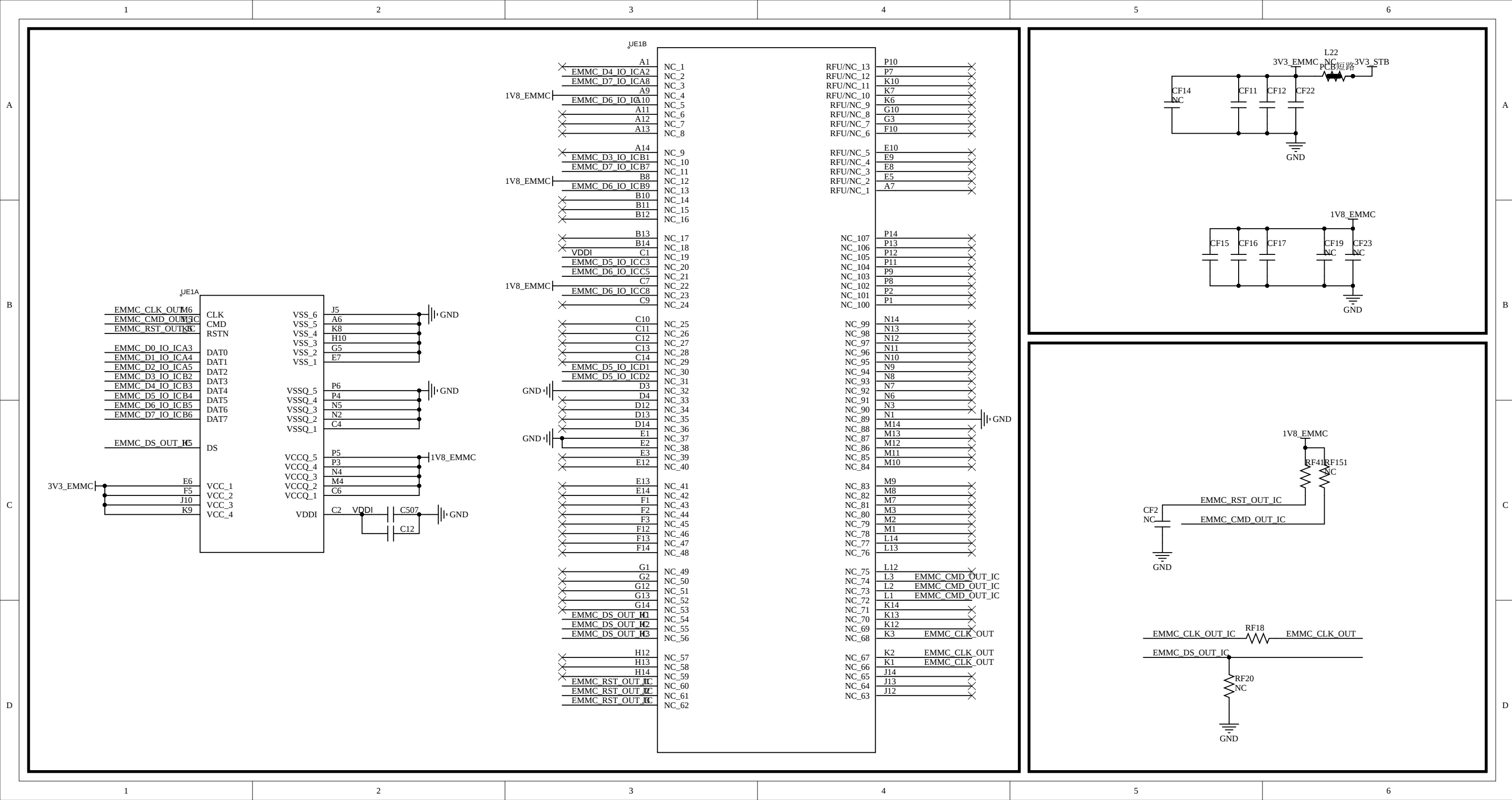


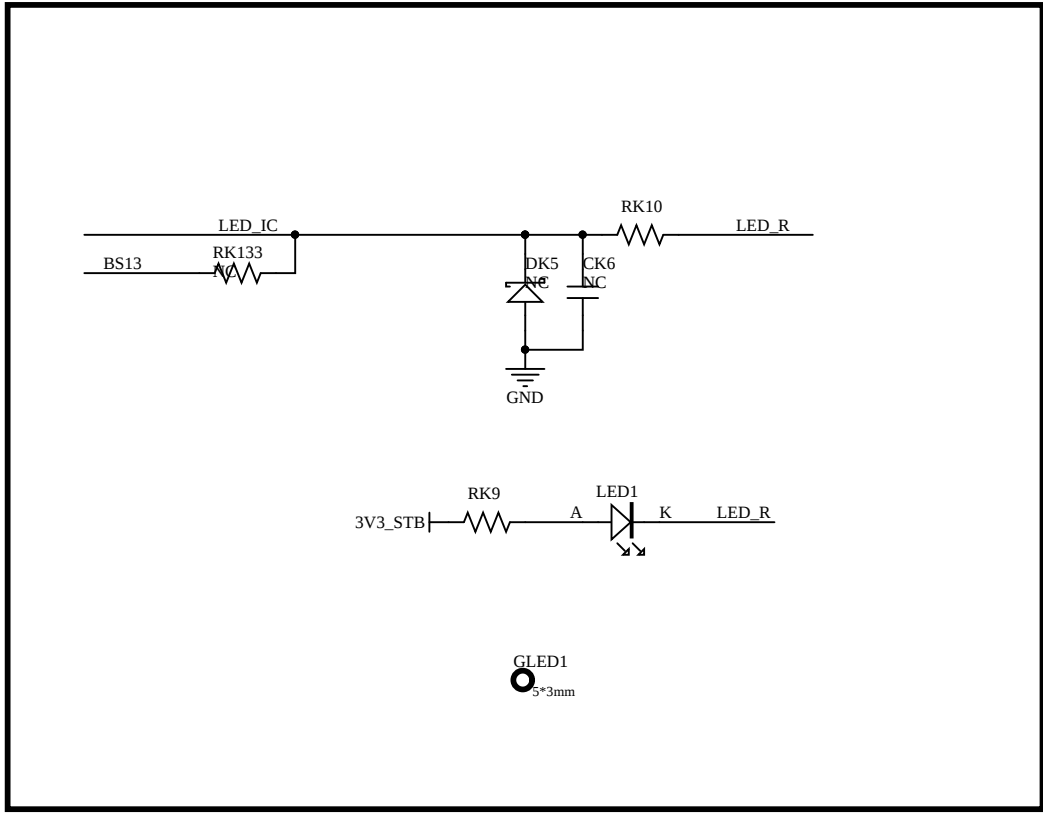
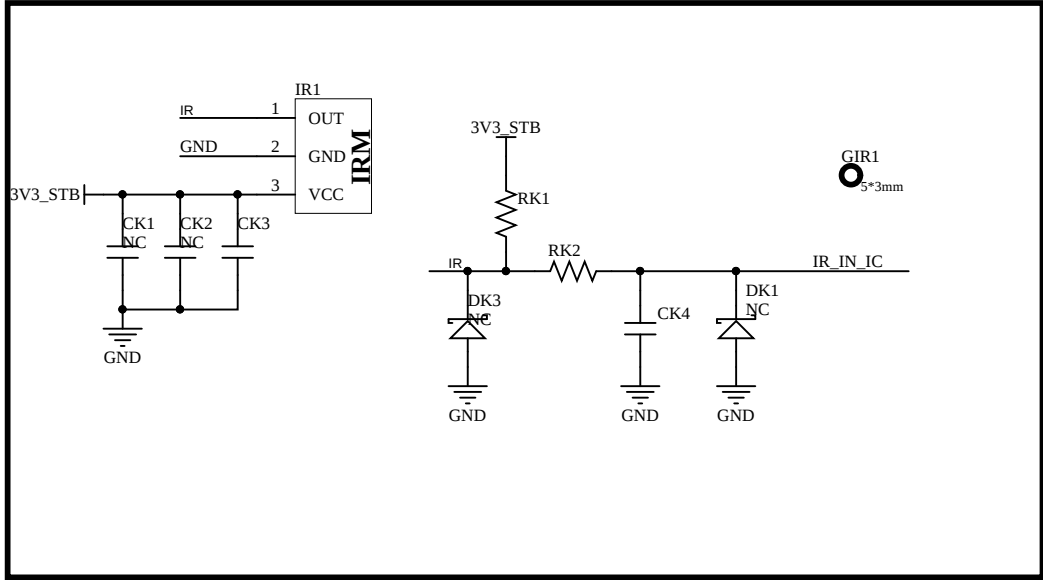
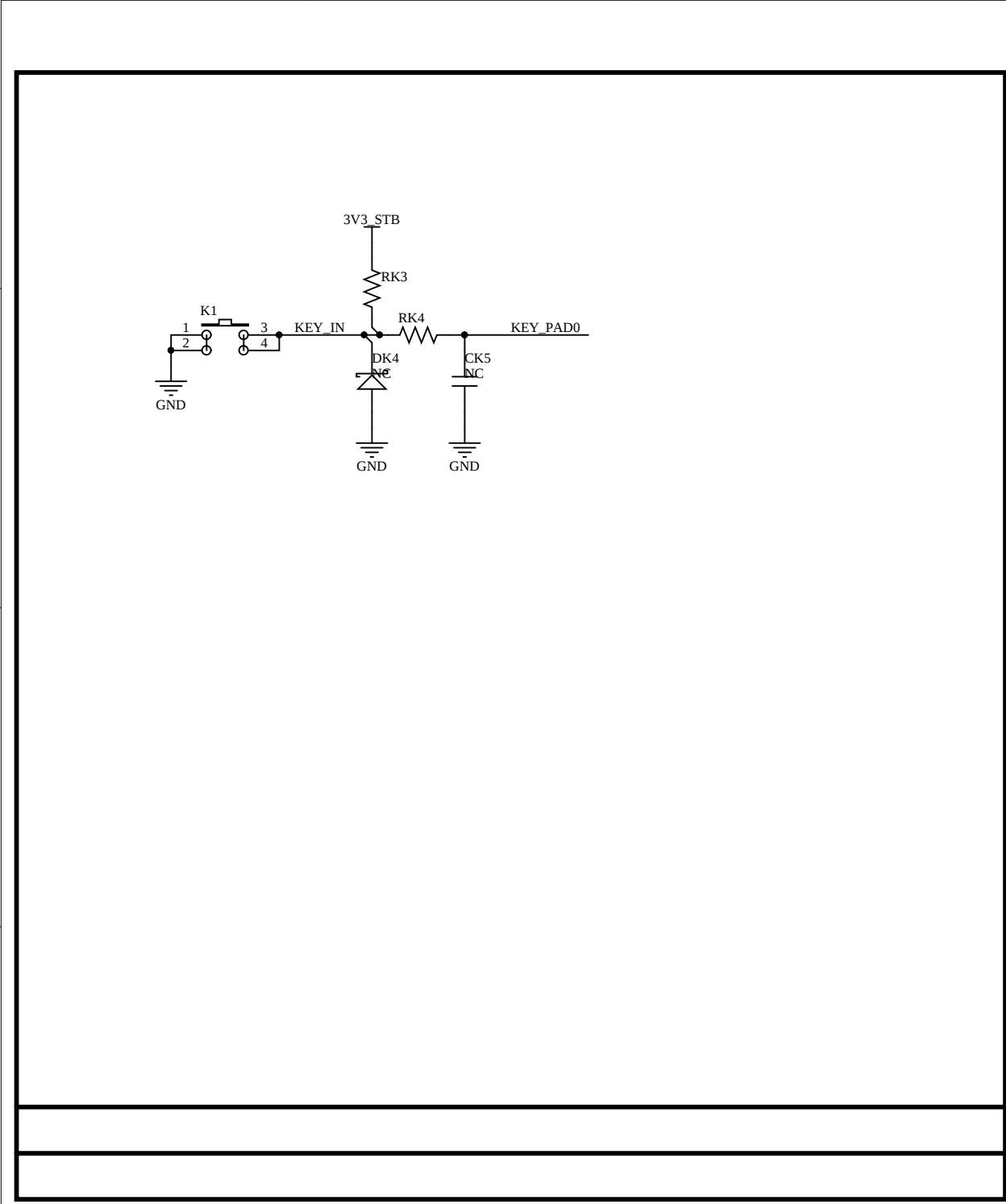


NAMING RULE
CA DDR4/DDR3
DQ DDR4_16BITS/DDR3 16BITS

U1A







A

B

C

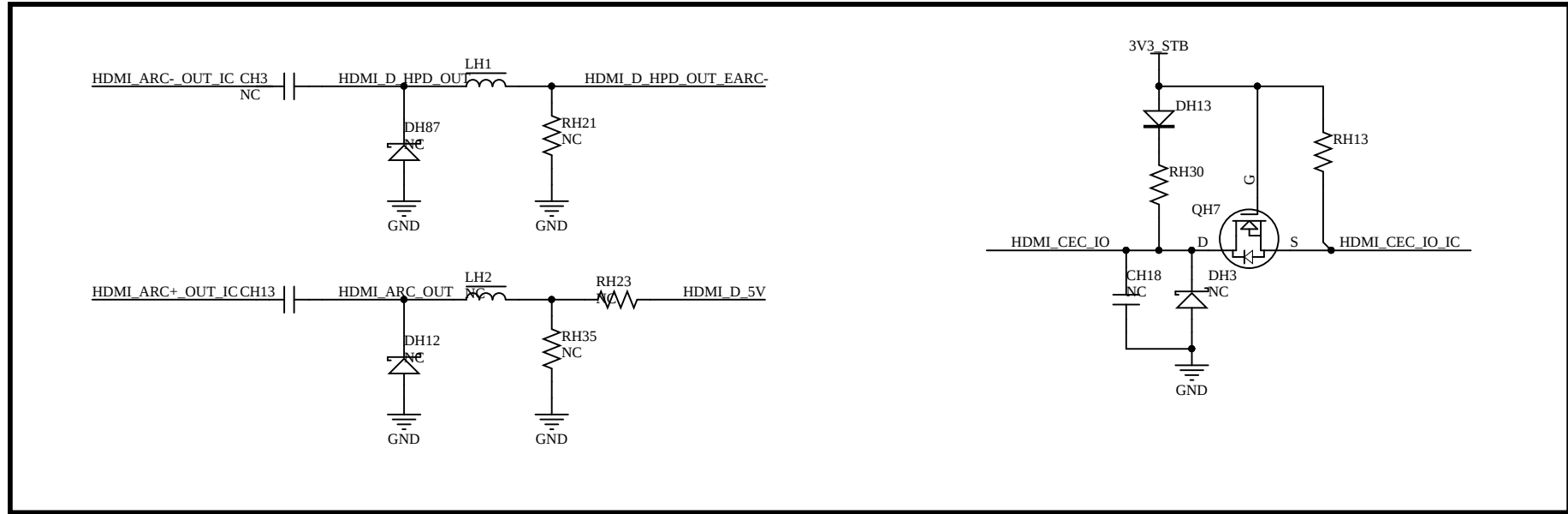
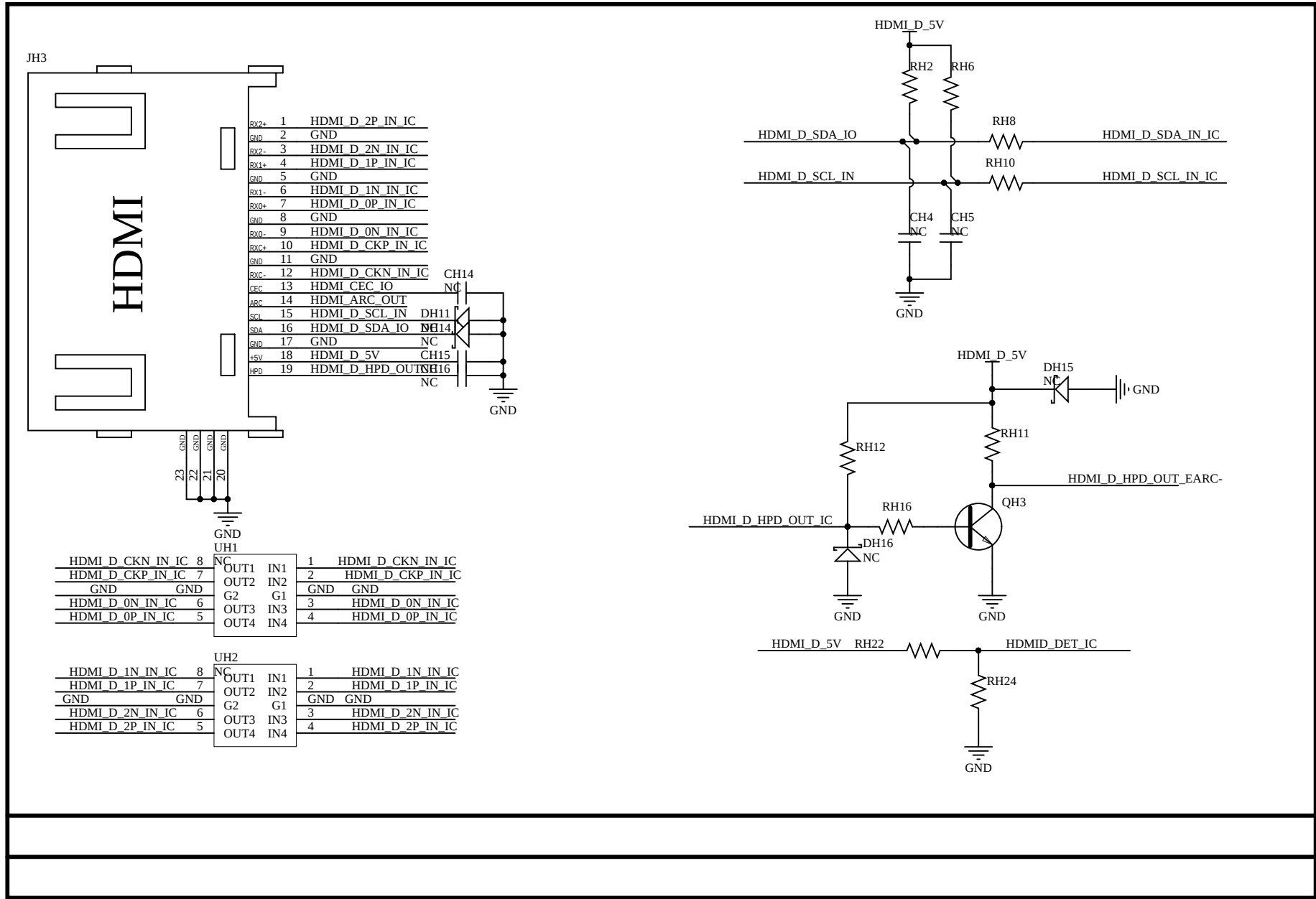
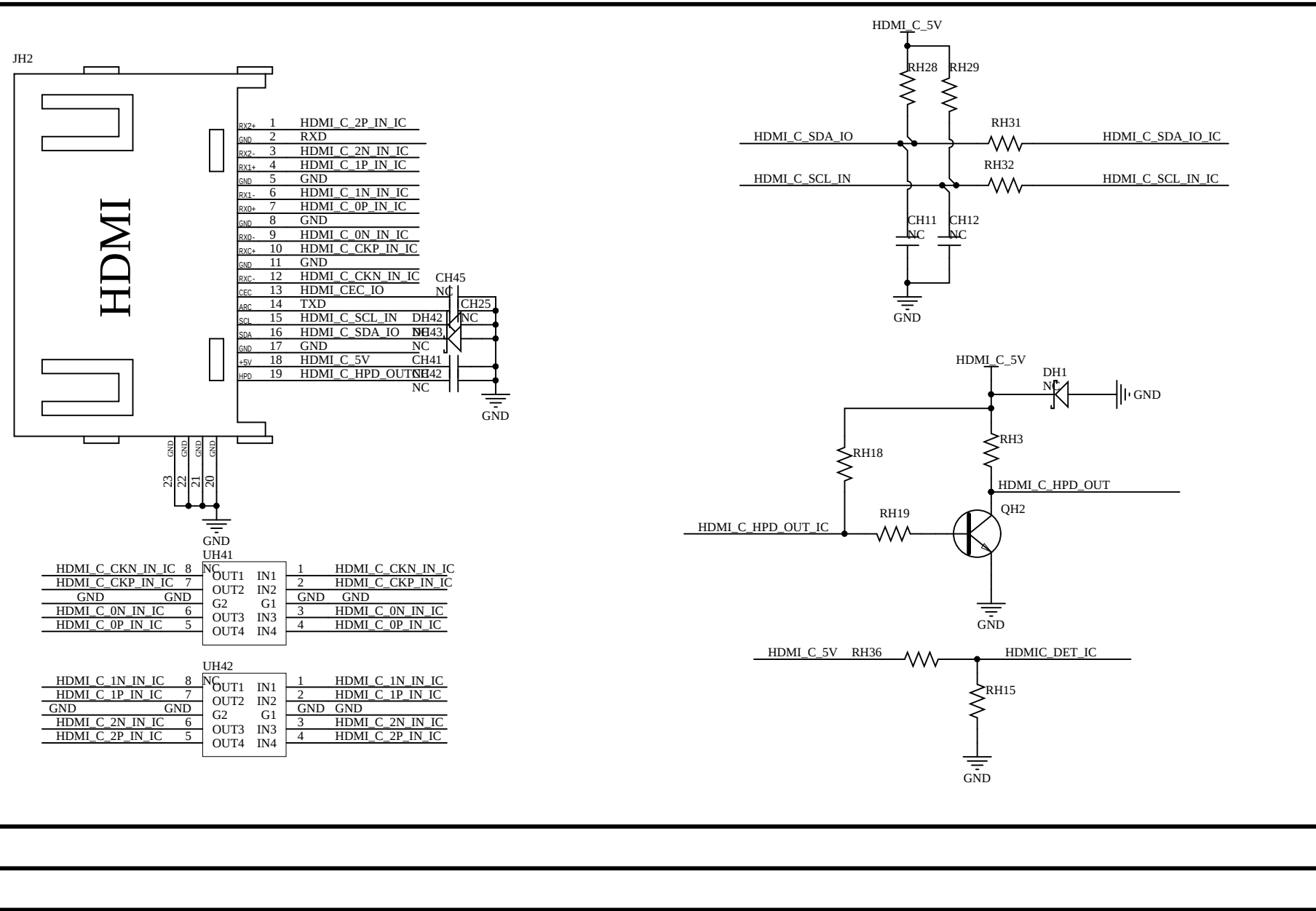
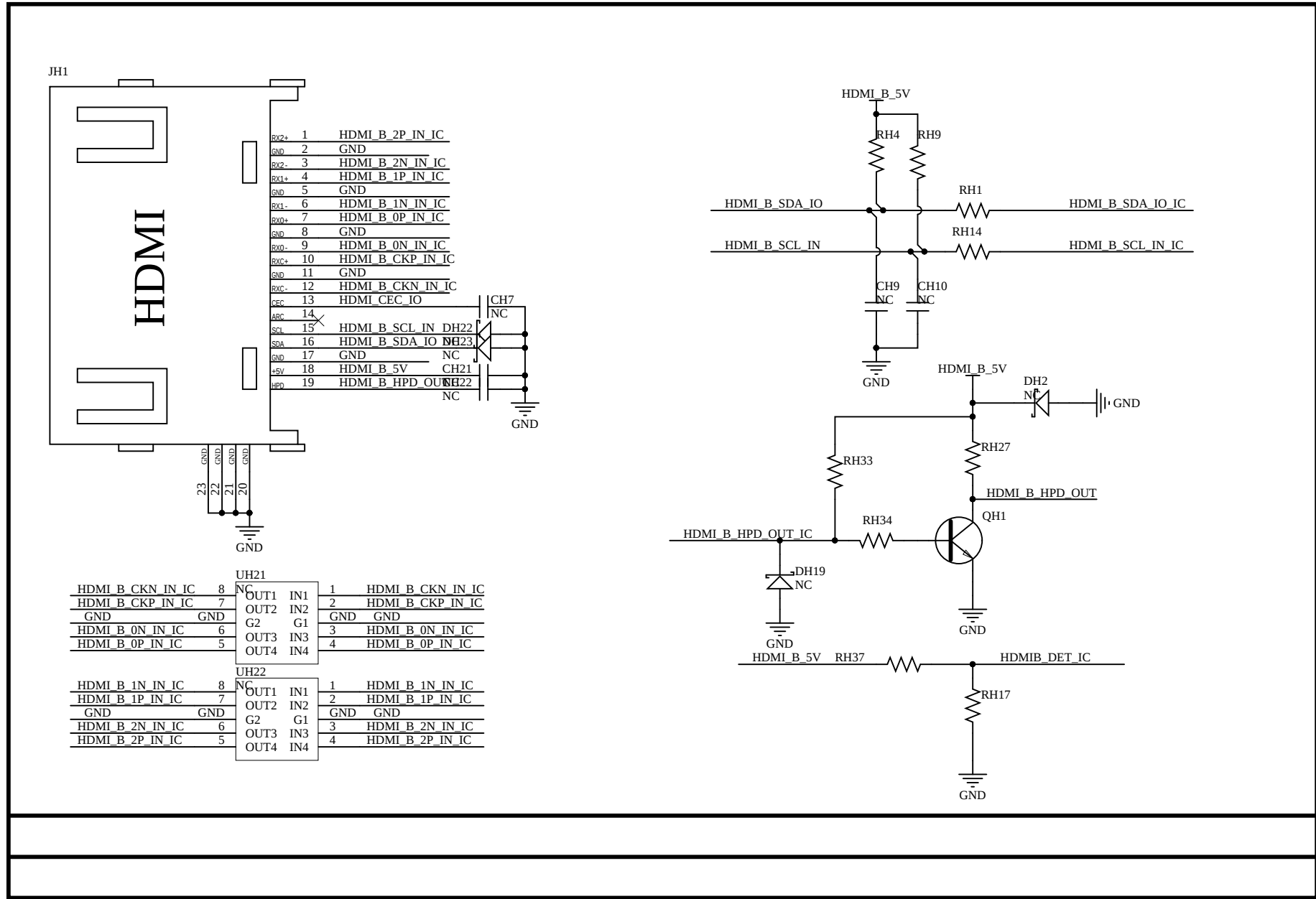
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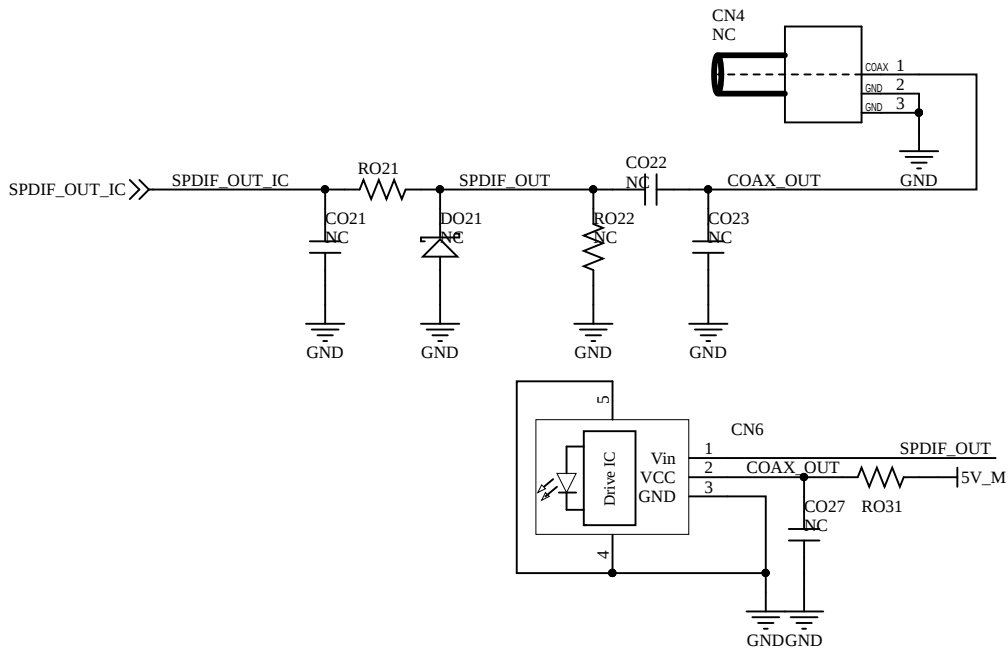
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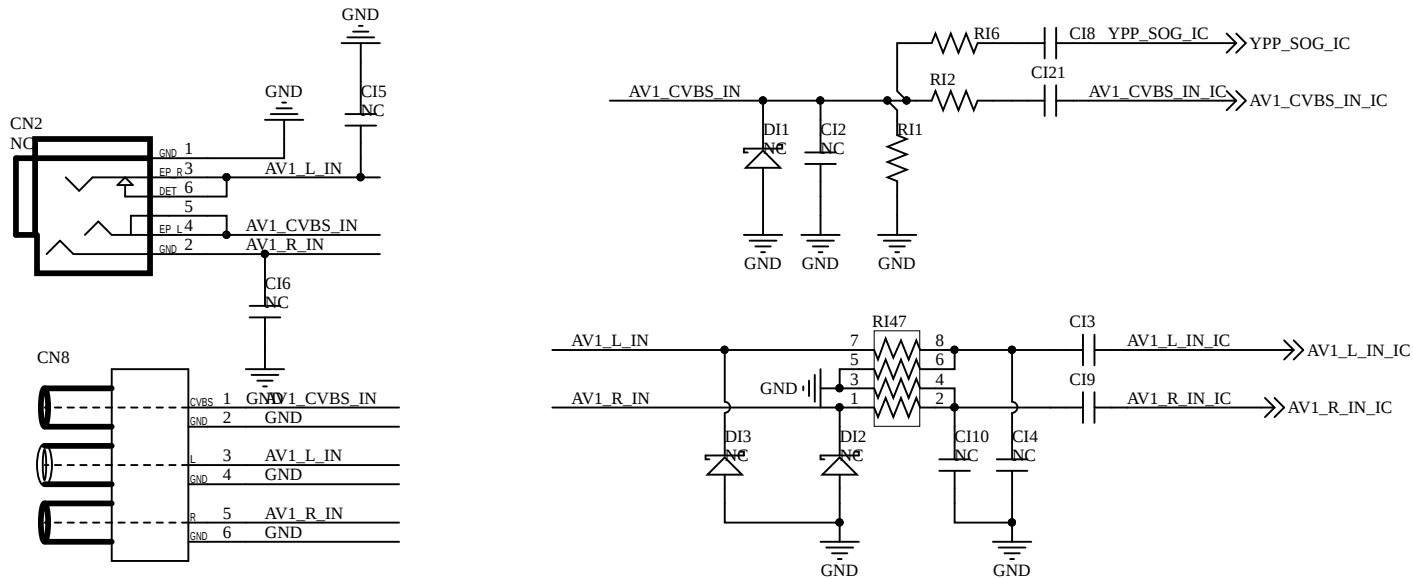


A

A

B

B

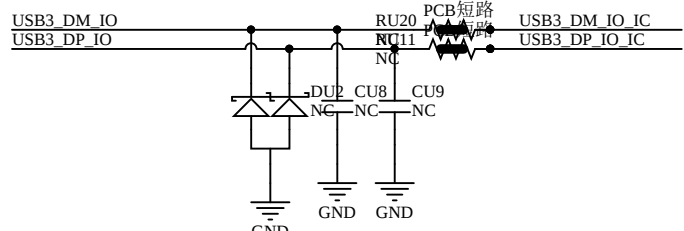
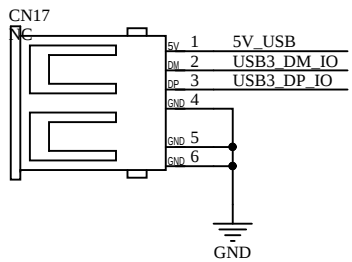
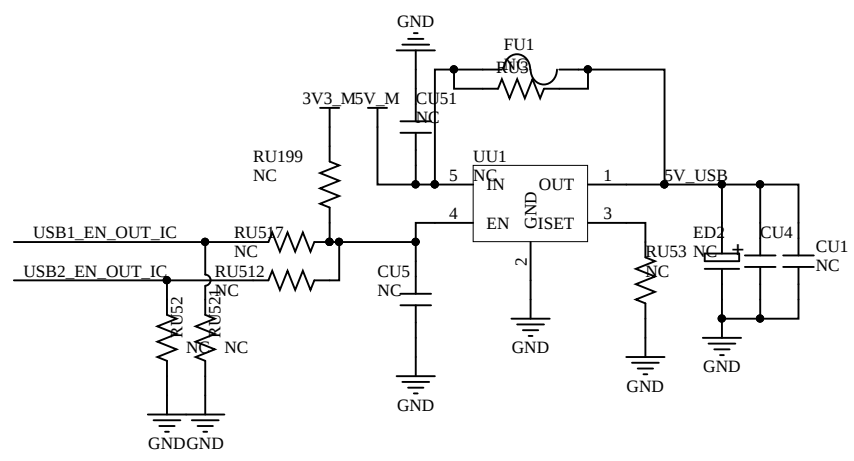
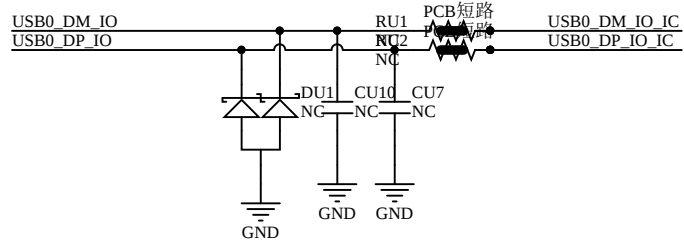
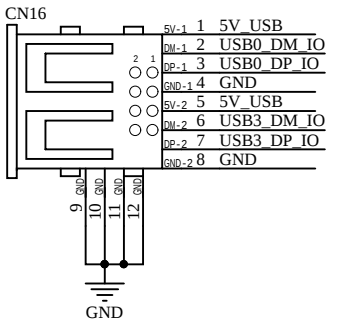
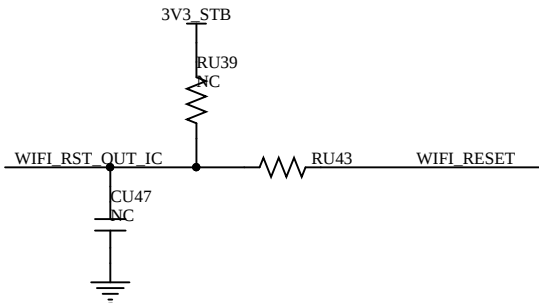
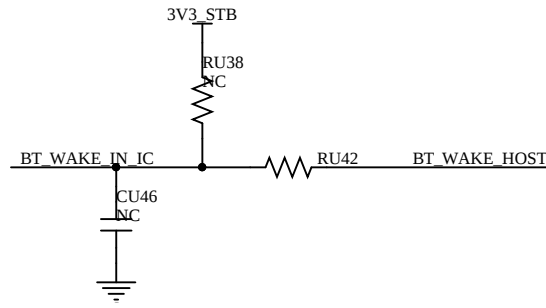
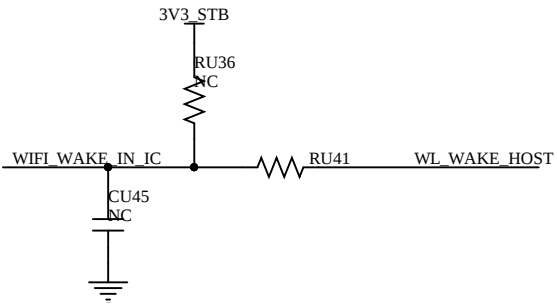
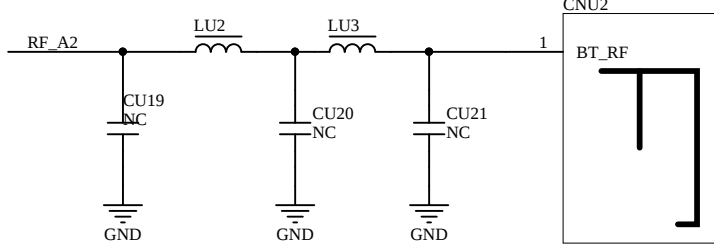
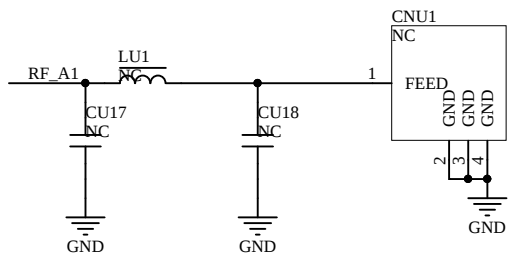
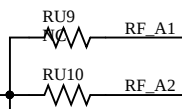
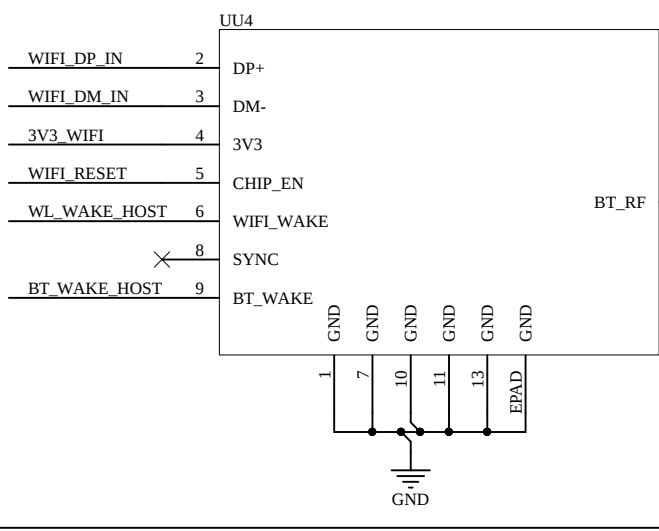
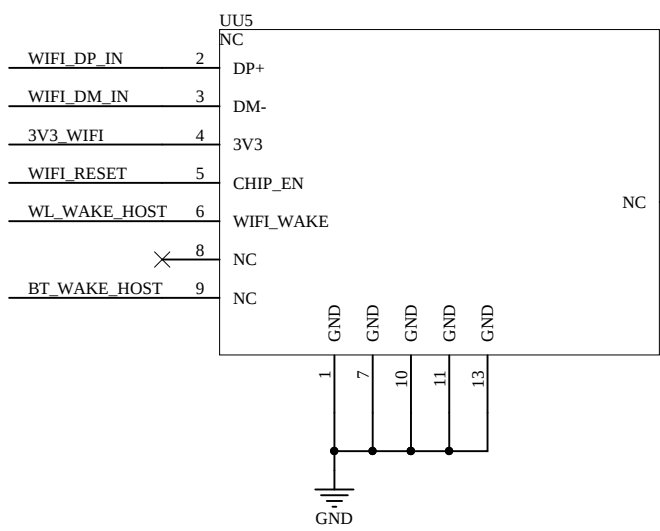
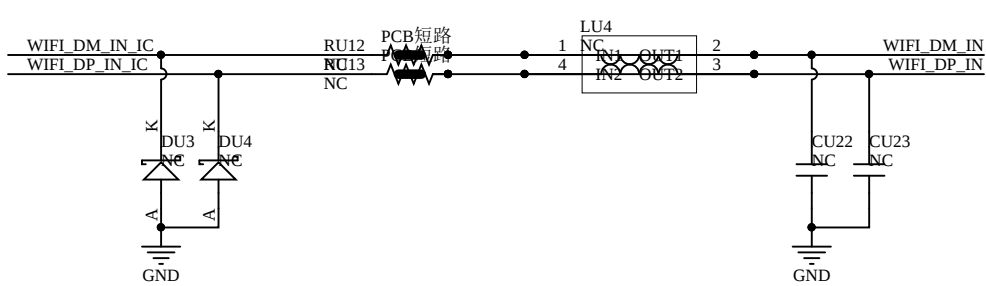
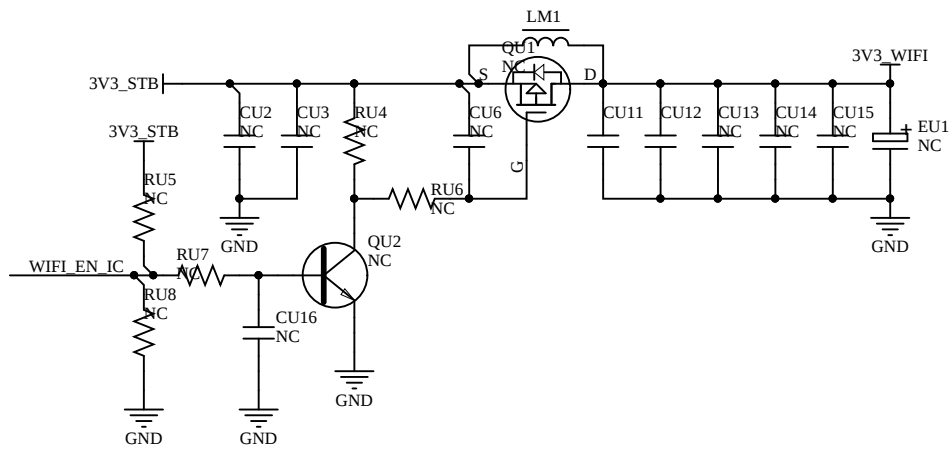


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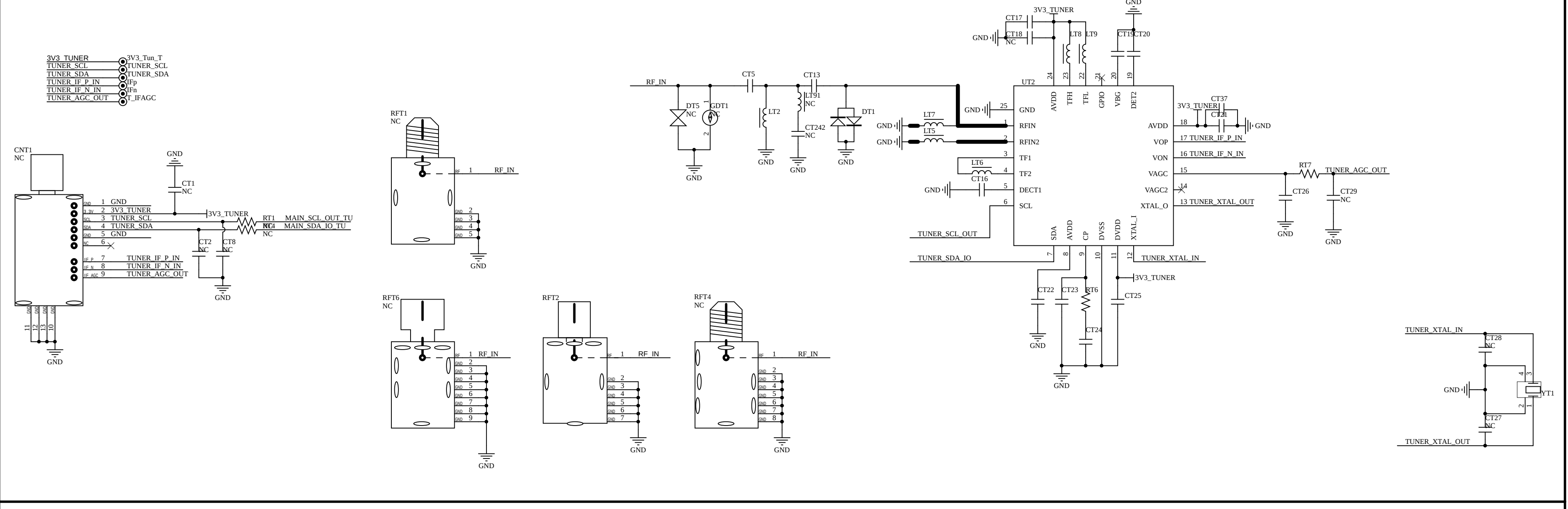
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D

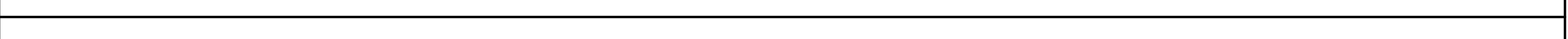


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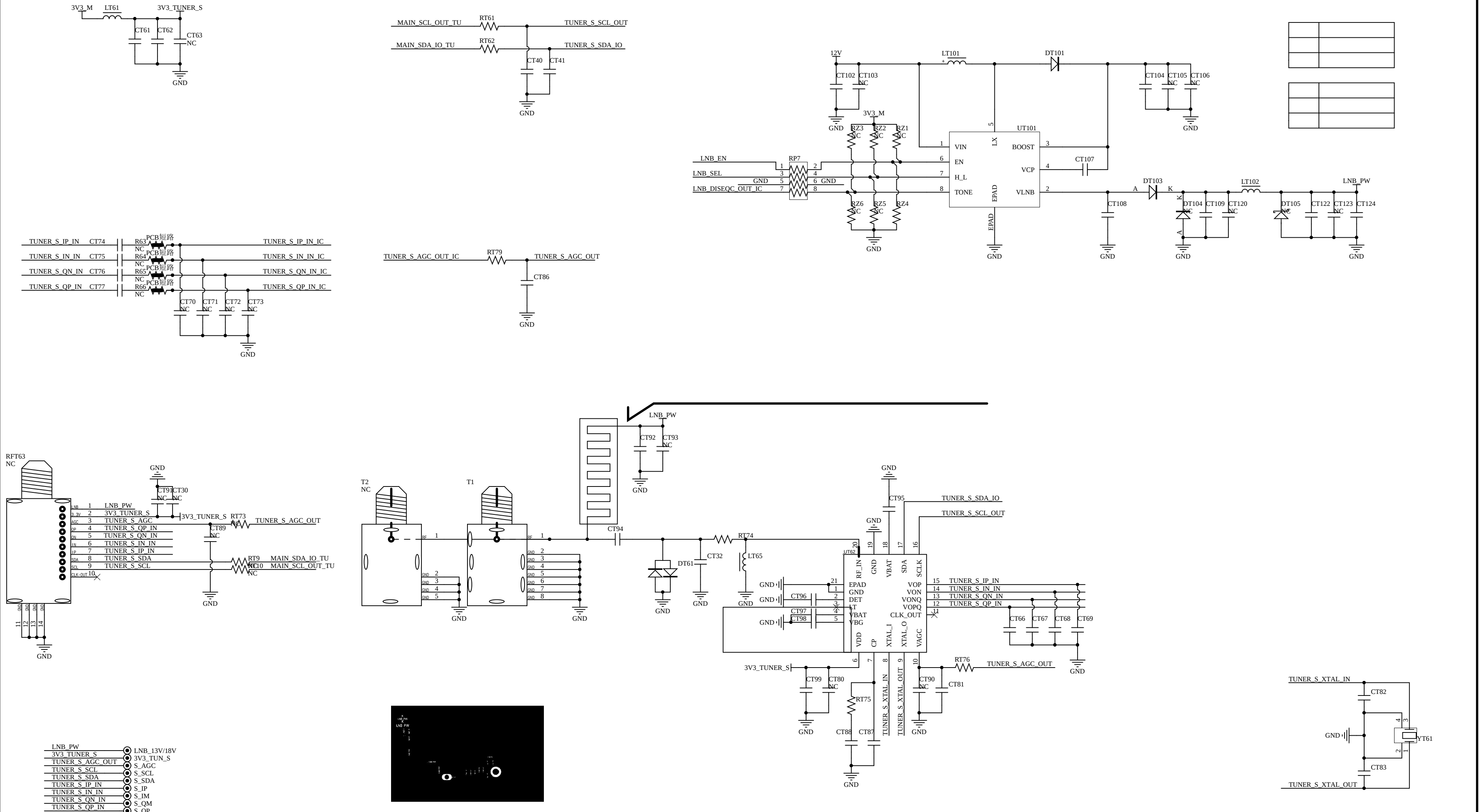


A

B



B

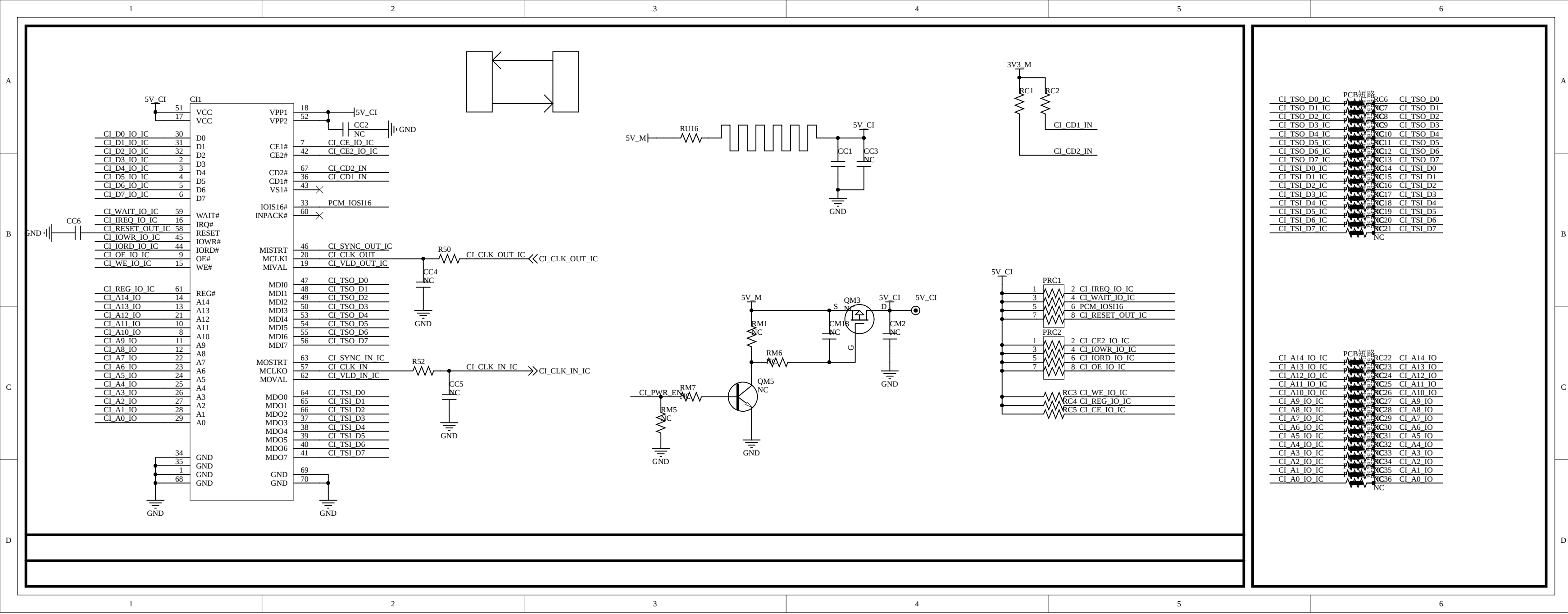


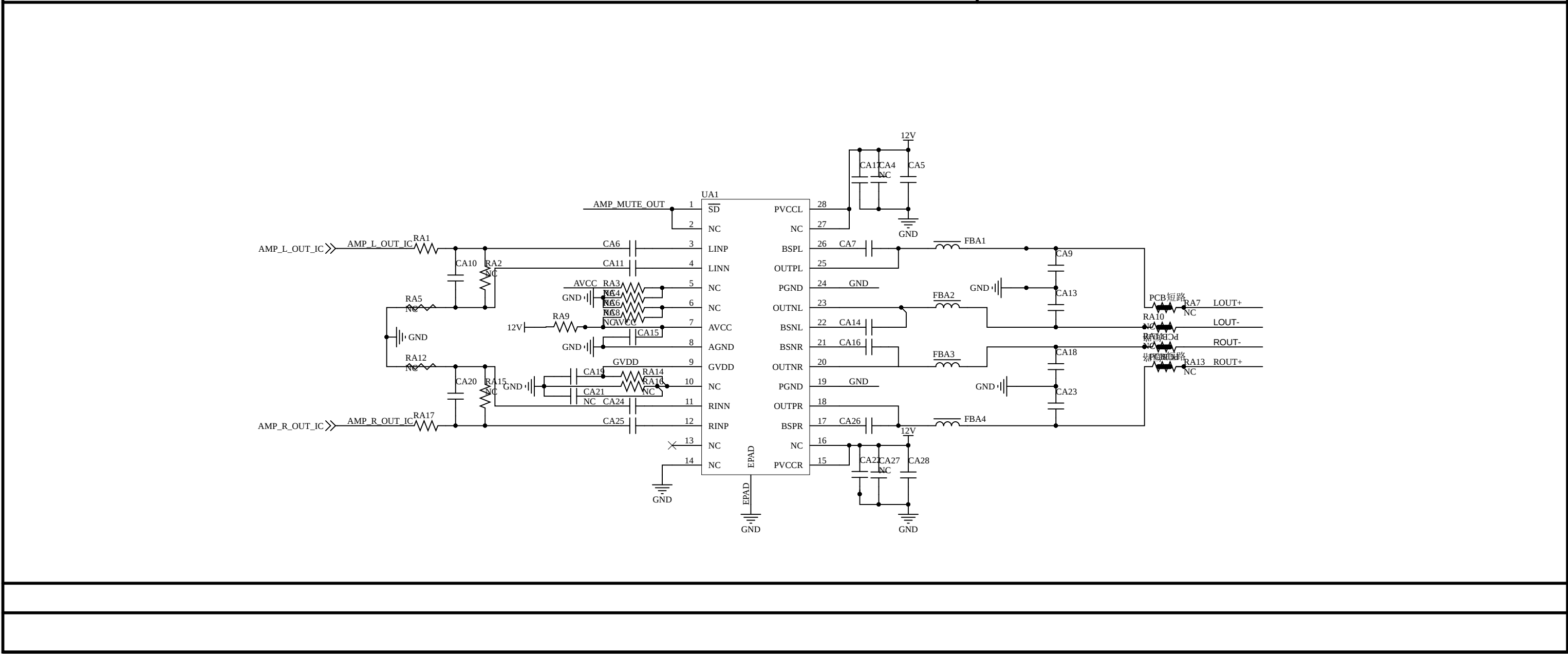
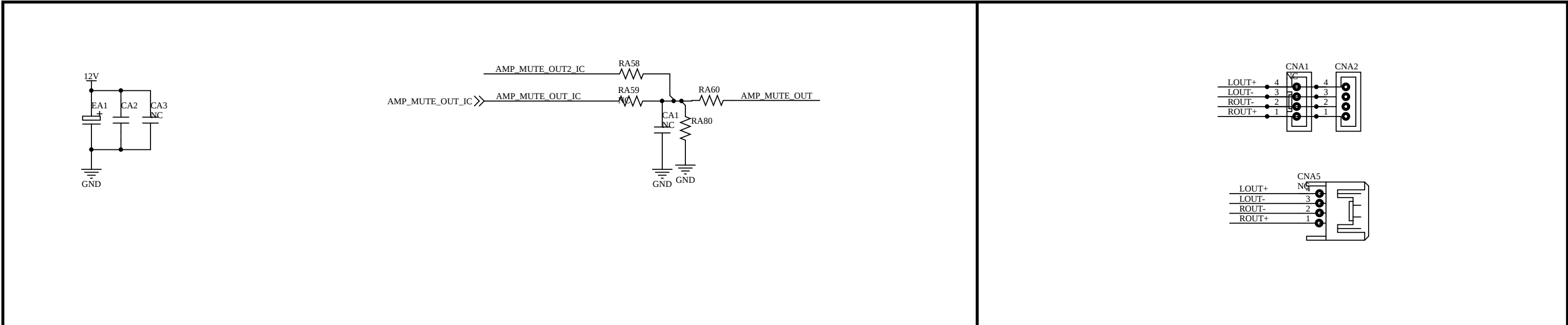
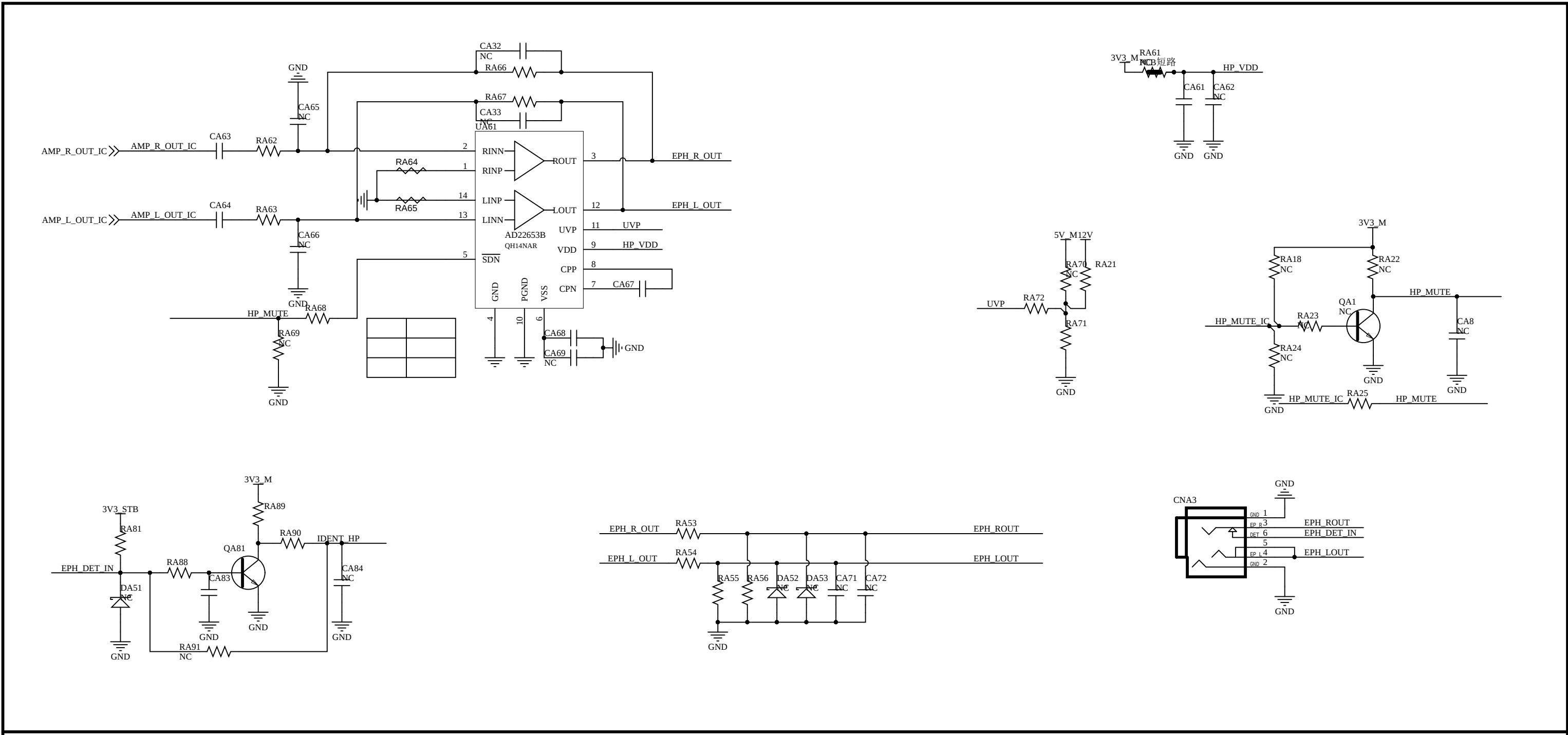
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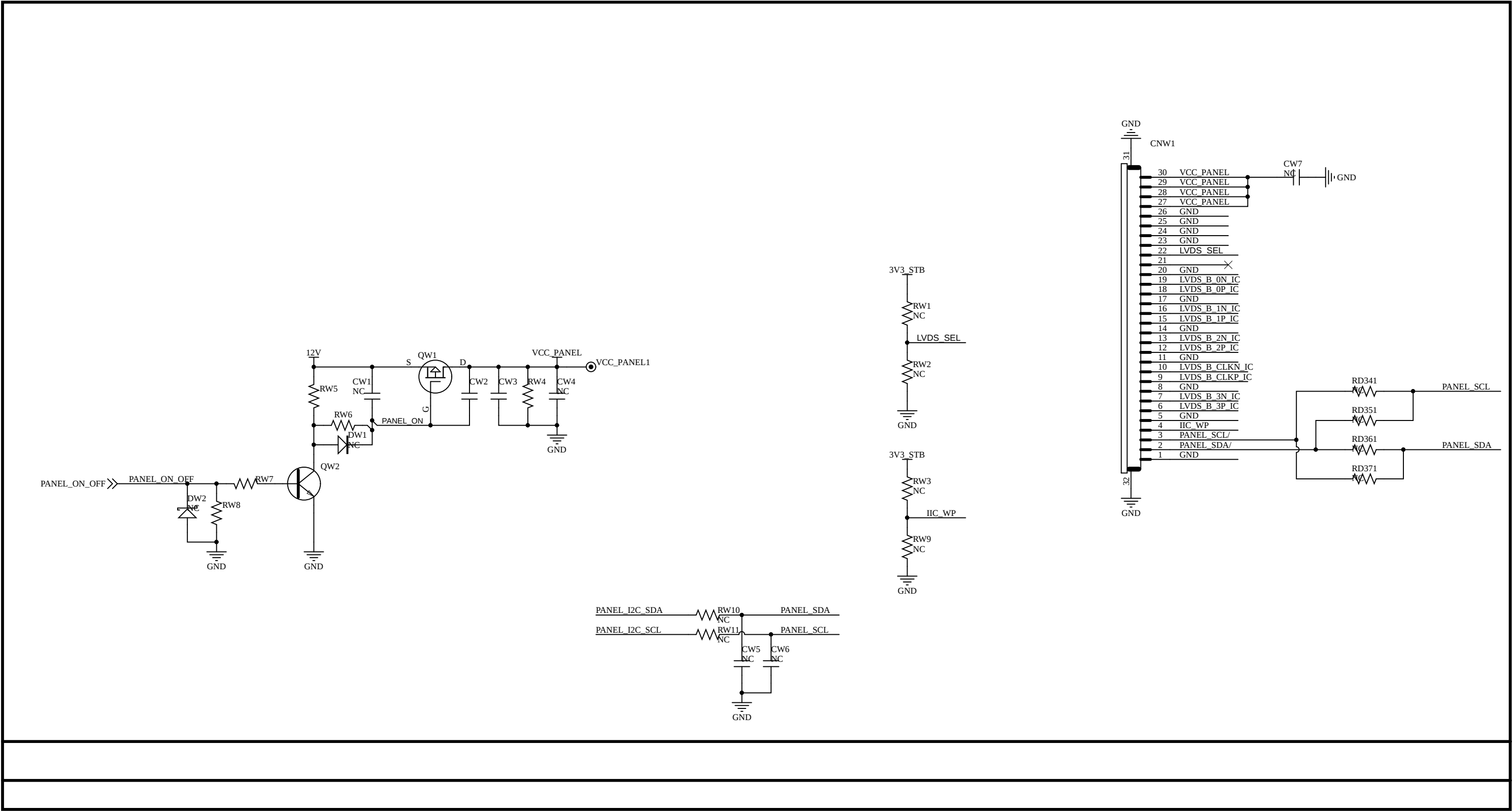


D





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